



**EULYNX Initiative**

## **Requirements specification for SCI-ILS**

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ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.1145	Head	<b>1 Introduction</b>		Default		
Eu.ILS.1147	Head	<b>1.1 Release information</b>		Default		
Eu.ILS.1148	Info	[Eu.Doc.41] Requirements specification for SCI-ILS CENELEC Phase: 4 Version: 4.3 (1.A) Approval date: 02.06.2025		Default		<b>Object Text:</b> [Eu.Doc.41] Requirements specification for SCI-ILS CENELEC Phase: 4 Version: 4.3 (01.A) Approval date: 2902.0506.20242025
Eu.ILS.1149	Info	<b>Version history</b>		Default		
Eu.ILS.4165	Info	version number: 4.0 (0.A) date: 16.05.2022 author: Dennis Kunz, Martin Herz model version: 18 Generic interface and subsystem requirements for SCI version: 1.0 (0.A) review: CCB changes: EUILS-268, EUILS-269, EUILS-270, EUILS-271, EUILS-272, EUILS-273, EUILS-274		Default		
Eu.ILS.4177	Info	version number: 4.1 (0.A) date: 05.04.2023 author: Dennis Kunz model version: 21 Generic interface and subsystem requirements for SCI version: 1.0 (1.A) review: cluster changes: EUILS-278, EUILS-279, EUILS-280, EUILS-282, EUILS-283		Default		
Eu.ILS.4215	Info	version number: 4.2 (0.A) date: 26.06.2023 author: Dennis Kunz model version: 22 Generic interface and subsystem requirements for SCI version: 1.0 (3.A) review: CCB changes: EUILS-279, EUILS-284, EUILS-285, EUILS-286, EUILS-287, EUILS-290, EUILS-292		Default		
Eu.ILS.4222	Info	version number: 4.2 (1.A) date: 15.12.2023 author: Dennis Kunz model version: 25 Generic interface and subsystem requirements for SCI version: 1.0 (4.A) review: M&T changes: EUILS-277, EUILS-296, EUILS-297, EUILS-298, EUILS-299, EUILS-300, EUILS-301		Default		
Eu.ILS.4249	Info	version number: 4.2 (2.B) date: 30.04.2024 author: Dennis Kunz model version: 26 Generic interface and subsystem requirements for SCI version: 1.0 (5.A) review: cluster changes: EUILS-276, EUILS-305, EUILS-307, EUILS-309, EUILS-311, EUILS-312		Default		
Eu.ILS.4271	Info	version number: 4.3 (0.A) date: 18.06.2024 author: Dennis Kunz model version: 26 Generic interface and subsystem requirements for SCI version: 1.1 (0.A) review: CCB changes: EUILS-313, EUILS-314		Default		<b>Object Text:</b> version number: 4.3 (0.A) date: 18.06.2024 author: Dennis Kunz model version: 26 Generic interface and subsystem requirements for SCI version: 1.1 (0.A) review: <del>FACS-</del> <del>Mirror-Group</del> CCB changes: EUILS-313, EUILS-314
Eu.ILS.4272	Info	version number: 4.3 (1.A) date: 19.06.2025 author: Dennis Kunz model version: 29 Generic interface and subsystem requirements for SCI version: 1.1 (2.A) review: CCB changes: EUILS-320, EUILS-321		Default		object created after baseline 4.3 (0.A)
Eu.ILS.1152	Head	<b>1.2 Impressum</b>		Default		

ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.1153	Info	<div>Publisher: <b>EULYNX Initiative</b></div> <div>A full list of the <b>EULYNX Partners</b> can be found on <a href="https://eulynx.eu/">https://eulynx.eu/</a>.</div>		Default		
Eu.ILS.1154	Info	<div>Responsible for this document: EULYNX Project Management Office <a href="http://www.eulynx.eu">www.eulynx.eu</a></div>		Default		
Eu.ILS.1189	Info	<div>Copyright EULYNX Partners</div> <div>All information included or disclosed in this document is licensed under the European Union Public Licence EUPL, Version 1.2 or later.</div>		Default		
Eu.ILS.1155	Head	<b>1.3 Purpose</b>		Default		
Eu.ILS.1156	Info	The purpose of the document is the specification of requirements for the interface SCI-ILS for the development of the EULYNX System.		Default		
Eu.ILS.1160	Info	This document describes functional requirements for the interface SCI-ILS.		Default		
Eu.ILS.1161	Info	<div>This document is intended for the following users:</div> <ul style="list-style-type: none"><li>• safety authorities</li><li>• infrastructure managers</li><li>• safety accessors</li><li>• signalling system suppliers</li><li>• validators</li></ul>		Default		
Eu.ILS.1162	Info	This document is the basis for the implementation by the supplier and for approval by the infrastructure manager.		Default		
Eu.ILS.1164	Head	<b>1.4 Applicable standards and regulations</b>		Default		
Eu.ILS.1163	Info	A list of applicable standards and regulations used in EULYNX is listed in the EULYNX Reference Document List [Eu.Doc.12].		Default		
Eu.ILS.1165	Head	<b>1.5 Applicable documents</b>		Default		
Eu.ILS.1166	Info	The current versions of documents used as input or related to this document are listed in the EULYNX Documentation Plan [Eu.Doc.11]. The relationships between the documents are displayed in the Appendix A1 Documentation plan and structure [Eu.Doc.11_A1].		Default		
Eu.ILS.1179	Head	<b>1.6 Terms and abbreviations</b>		Default		
Eu.ILS.1180	Info	The terms and abbreviations are listed in the EULYNX Glossary [Eu.Doc.9].		Default		
Eu.ILS.1186	Head	<b>1.7 Variability management</b>		Default		
Eu.ILS.1187	Info	Applicability column indicates the applicability of the requirement or information object per EULYNX partner. Value "Default" means the object applies to all EULYNX partners. Value "IM code" means the object applies specifically to the stated EULYNX partner. Value "-" indicates that this requirement is part of the chapters of the state machine modelling. The state machine itself defines the applicability of each transition. If there are no FlowPorts which describe the different applicabilities, the whole state machine is default. IM codes follow the pattern "abcdyz", where abcd is the UIC numeric code for railway companies and yz is by default "00".		Default		
Eu.ILS.1285	Head	<b>1.8 Definition of object types</b>		Default		
Eu.ILS.1286	Info	The following definition for object types is applied in this document:		Default		
Eu.ILS.1287	Info	<ul style="list-style-type: none"><li>• "Req" - This denotes a mandatory requirement.</li></ul>		Default		
Eu.ILS.4223	Info	<ul style="list-style-type: none"><li>• "Def" - This denotes referenceable model elements that are used in the model-based creation of requirements</li></ul>		Default		
Eu.ILS.1288	Info	<ul style="list-style-type: none"><li>• "Info" - This denotes additional information to help understand the specification. These objects do not specify any additional requirements.</li></ul>		Default		
Eu.ILS.1289	Info	<ul style="list-style-type: none"><li>• "Head" - This denotes chapter headings.</li></ul>		Default		
Eu.ILS.1181	Head	<b>1.9 Modelling</b>		Default		
Eu.ILS.1182	Info	The section "Functional requirements specification" follows a model based systems engineering process using Systems Modelling Language (SysML) and defines the information objects (stimuli and responses) exchanged over the SCI-ILS interface.		Default		
Eu.ILS.1184	Info	The diagrams presented in this document are modelled in SysML [SysML].		Default		
Eu.ILS.1355	Info	The rules for the interpretation of the model based parts of specification are defined in [Eu.Doc.29].		Default		
Eu.ILS.1352	Info	In chapter 3 "Functional requirements specification" the functional system requirements, defined in the form of a SysML model in the PTC Integrity Modeler are depicted as a surrogate of this model in the form of DOORS-objects.		Default		
Eu.ILS.1353	Info	A requirement thereby consists of the respective SysML model element, for instance a SysML diagram, and if necessary an additional extension of the requirement.		Default		
Eu.ILS.1354	Info	In the column “Requirement Part 1” the particular SysML model element is depicted and in the column “Requirement Part 2” the corresponding extension of the definition is given. The stated object type normally applies both to “Requirement Part 1” and to “Requirement Part 2”.		Default		
Eu.ILS.1356	Info	There are requirements with type "Req" given, where the column "Requirement Part 2" or a part of it is provided with the heading "Information". In this case, the defined type only applies to the column "Requirement Part 1" and the part of "Requirement Part 2", which is not labelled as "Information".		Default		
Eu.ILS.4224	Info	State machines or several state machines linked together in a Functional Architecture define the totality of all functional requirements of an SUS or an SIUS in a coherent and consistent manner. State diagrams of a corresponding state machine are marked with the object type “Req”. For the later design and implementation, it is not the description language SysML that is binding, but the domain-specific meaning expressed by it. The specified behaviour can be converted into a vendor specific language but must retain the domain specific meaning describing the functional requirements. The specific model elements are additionally specified and defined by object type “Def” to allow for traceability to supplier designs or test cases. The compliance of products to the specifications must be demonstrated by testing against EULYNX test cases, which are derived from the functionality specified by the models.		Default		
Eu.ILS.1144	Head	<b>2 Conditions of use</b>		Default		
Eu.ILS.1188	Info	The specifications defined in this document shall follow the requirements of the EULYNX System Architecture Specification [Eu.Doc.16].		Default		
Eu.ILS.1389	Req	The specifications defined in this document shall be complemented by the generic requirements specified in Generic interface and subsystem requirements for SCI [Eu.Doc.119].		Default		
Eu.ILS.3894	Req	All references to [Eu.Doc.119] refer to version 1.1 of that document.		Default	EUILS-320	<b>Object Text:</b> All references to [Eu.Doc.119] refer

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						to version 1.1 <del>(0.A)</del> of that document. <b>a_JIRA_BL4R4:</b> <a href="#">EUILS-320</a>
Eu.ILS.1311	Head	<b>2.1 General</b>		Default		
Eu.ILS.1297	Info	The SCI-ILS is specified to be used in stations and on the line. Detailed applications are defined by national requirements. Further explanations regarding the terminology and intended applications of SCI-ILS are described in chapter 3 and 7 of [Eu.Doc.10].		Default		
Eu.ILS.1300	Info	The adjacent system Adjacent Interlocking System can be a Subsystem - Electronic Interlocking of another EULYNX System or a legacy interlocking system.		Default		
Eu.ILS.1301	Info	If the adjacent system Adjacent Interlocking System is a legacy interlocking system then it also shall support the SCI-ILS to communicate with the EULYNX System.		Default		
Eu.ILS.1298	Info	The SCI-ILS uses one Process Data Interface protocol connection between the Subsystem - Electronic Interlocking of the EULYNX System and the adjacent system Adjacent Interlocking System.		Default		
Eu.ILS.1303	Info	Through this one Process Data Interface protocol connection, the SCI-ILS handles multiple tracks and multiple interlocking system boundaries with one boundary for each track. Requirements regarding the configuration of the boundaries are defined in chapter 4. Further information about interlocking system boundaries including explaining track plans is given in [Eu.Doc.10].		Default		
Eu.ILS.3896	Info	The SCI-ILS is an interface between two equal partners. The actors Subsystem - Electronic Interlocking and Adjacent Interlocking System can be interchanged, depending on the point of view.		Default		
Eu.ILS.4005	Info	In the Use Case 'Initialisation', configuration data must define which of the two equal partners acts as primary, respectively secondary communication partner. In this document, the primary communication partner is always shown as the Adjacent Interlocking System, the secondary communication partner is always shown as Subsystem - Electronic Interlocking.		Default		
Eu.ILS.4173	Head	<b>2.2 Border Interfaces</b>		Default		
Eu.ILS.4174	Info	When EULYNX is applied on a cross border railway line, EULYNX interfaces can exist between subsystems or adjacent systems that belong to different IMs.		Default		
Eu.ILS.4175	Info	To ensure consistent communication and functionality, a set of harmonised requirements to describe cross border interface functionality shall be agreed by both IMs.		Default		
Eu.ILS.4176	Info	The subset marker "yz" of the IM code can be used to mark applicability information consistent with the cross border interface functionality by each IM.		Default		
Eu.ILS.3	Head	<b>3 Functional requirements specification</b>		Default		
Eu.ILS.1829	Head	<b>3.1 SCI-ILS - Logical Viewpoint</b>		Default		
Eu.ILS.32	Def	<div><div>[Package] SCI-ILS - Logical Context [Logical Viewpoint - Interface Definition]</div><div><div><b>bdd</b> [Package] SCI-ILS - Logical Context [Logical Viewpoint - Interface Definition]</div><div><div><div><div>Subsystem - Electronic Interlocking</div><div><div>«logical structural entity» Subsystem - Electronic Interlocking</div><div>1</div></div><div>SCI-ILS</div></div><div><div>«logical structural entity» SCI-ILS</div><div></div></div><div><div>Adjacent Systems and System Actors</div><div><div>«environmental structural entity» Adjacent Interlocking System</div><div>1</div></div><div>SCI-ILS</div></div></div><div>SCI-ILS</div></div></div></div>		Default		
Eu.ILS.4270	Req	The logical interface SCI-ILS shall provide a connection of exactly one Subsystem - Electronic Interlocking and one Adjacent Interlocking System.		Default		
Eu.ILS.5	Head	<b>3.2 SCI-ILS - Information Flows</b>		Default		
Eu.ILS.4172	Info	The generic commands and messages through the SCI-ILS are specified in [Eu.Doc.119].		Default		

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Eu.ILS.4008	Def	<div><div>[Package] SCI-ILS - Information Flows [Interface Requirements - Directions Of Exchanged Information Objects (Internal)]</div><div><b>bdd</b> [Package] SCI-ILS - Information Flows [Interface Requirements - Directions Of Exchanged Information Objects (Internal)]</div><div><div>«information flow» SCI_ILS_Internal</div><div>provreqd «signal» Internal_Cd_Abort_Route_Cancellation_Request provreqd «signal» Internal_Cd_Access_Restriction_Request provreqd «signal» Internal_Cd_Flank_Protection_Request provreqd «signal» Internal_Cd_Route_Cancellation_Request provreqd «signal» Internal_Cd_Route_Prestest_Request provreqd «signal» Internal_Cd_Route_Release_Inhibition_Activation_Request provreqd «signal» Internal_Cd_Route_Request provreqd «signal» Internal_Msg_Access_Restriction_Status provreqd «signal» Internal_Msg_Activation_Zone_Status provreqd «signal» Internal_Msg_Approach_Zone_Status provreqd «signal» Internal_Msg_Flank_Protection_Status provreqd «signal» Internal_Msg_Line_Direction_Control provreqd «signal» Internal_Msg_Line_Status provreqd «signal» Internal_Msg_Opposite_Main_Signal_Status provreqd «signal» Internal_Msg_Route_Monitoring_Status provreqd «signal» Internal_Msg_Route_Prestest_Status provreqd «signal» Internal_Msg_Route_Release_Inhibition_Status provreqd «signal» Internal_Msg_Route_Status provreqd «signal» Internal_Msg_Signal_Status provreqd «signal» Internal_Msg_Train_Operated_Route_Release_Status provreqd «signal» Internal_Msg_TVPS_Status provreqd «signal» Internal_Msg_TDP_Status</div></div></div>		Default		
Eu.ILS.4022	Def	Internal_Cd_Access_Restriction_Request		007000 007400 007800 007900 008200 310900		
Eu.ILS.4023	Def	Internal_Cd_Flank_Protection_Request		007000 007400 007800 007900 008400 008800 310900		
Eu.ILS.4024	Def	Internal_Cd_Route_Cancellation_Request		007000 007400 007800 007900 008000 008200 008400 008800 310900		
Eu.ILS.4025	Def	Internal_Cd_Route_Prestest_Request		007000 007400		
Eu.ILS.4026	Def	Internal_Cd_Route_Release_Inhibition_Activation_Request		007400		
Eu.ILS.4027	Def	Internal_Cd_Route_Request		Default		
Eu.ILS.4028	Def	Internal_Msg_Access_Restriction_Status		007000 007400 007800 007900 008000 008200 310900		
Eu.ILS.4029	Def	Internal_Msg_Activation_Zone_Status		Default		
Eu.ILS.4030	Def	Internal_Msg_Approach_Zone_Status		007000 007400 007800 007900 008000 008200		

ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
				008400 008800 310900		
Eu.ILS.4032	Def	Internal_Msg_Flank_Protection_Status		007000 007400 007800 007900 008400 008800 310900		
Eu.ILS.4033	Def	Internal_Msg_Line_Direction_Control		007000 007400 007800 007900 008000 008200 008400 008800 310900		
Eu.ILS.4034	Def	Internal_Msg_Line_Status		007000 007400 007800 007900 008000 008200 008400 008800 310900		
Eu.ILS.4035	Def	Internal_Msg_Opposite_Main_Signal_Status		007000 007800 007900 008800 310900		
Eu.ILS.4036	Def	Internal_Msg_Route_Monitoring_Status		007000 007400 007800 007900 008000 008200 008400 008800 310900		
Eu.ILS.4037	Def	Internal_Msg_Route_Prestest_Status		007000 007400		
Eu.ILS.4038	Def	Internal_Msg_Route_Release_Inhibition_Status		007400		
Eu.ILS.4039	Def	Internal_Msg_Route_Status		Default		
Eu.ILS.4040	Def	Internal_Msg_Signal_Status		007000 007400 007800 007900 008000 008200 008400 008800 310900		
Eu.ILS.4041	Def	Internal_Msg_Train_Operated_Route_Release_Status		Default		
Eu.ILS.4042	Def	Internal_Msg_TVPS_Status		Default		
Eu.ILS.4201	Def	Internal_Cd_Abort_Route_Cancellation_Request		008400		
Eu.ILS.4259	Def	Internal_Msg_TDP_Status		008400		

ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.3951	Def	<div><div>[Package] SCI-ILS - Information Flows [Interface Requirements - Directions Of Exchanged Information Objects (External)]</div><div><div><b>bdd</b> [Package] SCI-ILS - Information Flows [Interface Requirements - Directions Of Exchanged Information Objects (External)]</div><div><div><div><div>«information flow» SCI_ILS</div><div>provreqd «signal» Cd_Abort_Route_Cancellation_Request provreqd «signal» Cd_Access_Restriction_Request provreqd «signal» Cd_Flank_Protection_Request provreqd «signal» Cd_Route_Cancellation_Request provreqd «signal» Cd_Route_Request provreqd «signal» Cd_Route_Release_Inhibition_Activation_Request provreqd «signal» Cd_Route_Prestest_Request provreqd «signal» Msg_Access_Restriction_Status provreqd «signal» Msg_Activation_Zone_Status provreqd «signal» Msg_Approach_Zone_Status provreqd «signal» Msg_Flank_Protection_Status provreqd «signal» Msg_Line_Direction_Control provreqd «signal» Msg_Line_Status provreqd «signal» Msg_Opposite_Main_Signal_Status provreqd «signal» Msg_Route_Monitoring_Status provreqd «signal» Msg_Route_Prestest_Status provreqd «signal» Msg_Route_Release_Inhibition_Status provreqd «signal» Msg_Route_Status provreqd «signal» Msg_Signal_Status provreqd «signal» Msg_Train_Operated_Route_Release_Status provreqd «signal» Msg_TVPS_Status provreqd «signal» Msg_TDP_Status</div></div><div><div>«information flow» SCI_ILS_AIS</div><div>proxyPorts «ProxyPort» P101inout : SCI_ILS «ProxyPort» P1inout : PDI_GEN_ADJ</div></div><div><div>«information flow» SCI_ILS_EIL</div><div>proxyPorts «ProxyPort» P101inout : SCI_ILS «ProxyPort» P1inout : PDI_GEN_ADJ</div></div><div><div>«information flow» PDI_GEN_ADJ</div><div>prov «signal» Cd_PDI_Version_Check reqd «signal» Msg_PDI_Version_Check prov «signal» Cd_Close_PDI prov «signal» Cd_Initialisation_Request reqd «signal» Msg_Start_Initialisation reqd «signal» Msg_Reset_PDI provreqd «signal» Msg_Status_Report_Completed reqd «signal» Msg_Initialisation_Completed</div></div></div></div></div></div>		Default		
Eu.ILS.4194	Def	Cd_Abort_Route_Cancellation_Request	Command (Cd) from sender interlocking to receiver interlocking to request the abortion of a secondary route cancellation.	008400		
Eu.ILS.3953	Def	Cd_Access_Restriction_Request	Command (Cd) from sender interlocking to receiver interlocking to request the activation or deactivation of an access restriction to the track section.	007000 007400 007800 007900 008200 310900		
Eu.ILS.3954	Def	Cd_Flank_Protection_Request	Command (Cd) from sender interlocking to receiver interlocking to request the provision or cancellation of flank protection.	007000 007400 007800 007900 008400 008800 310900		
Eu.ILS.3955	Def	Cd_Route_Cancellation_Request	Command (Cd) from sender interlocking to receiver interlocking to request the cancellation of a secondary route.	007000 007400 007800 007900 008000 008200 008400 008800 310900		
Eu.ILS.3956	Def	Cd_Route_Prestest_Request	Command (Cd) from sender interlocking to receiver interlocking to request the pretest of a secondary route.	007000 007400		
Eu.ILS.3957	Def	Cd_Route_Release_Inhibition_Activation_Request	Command (Cd) from sender interlocking to receiver interlocking to request the activation of the inhibited route release.	007400		
Eu.ILS.3958	Def	Cd_Route_Request	Command (Cd) from sender interlocking to receiver interlocking to request the initialisation of a secondary route.	Default		



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Eu.ILS.3959	Def	Msg_Access_Restriction_Status	Message (Msg) from sender interlocking to receiver interlocking to report the status of an access restriction to the track section.	007000 007400 007800 007900 008000 008200 310900		
Eu.ILS.3960	Def	Msg_Activation_Zone_Status	Message (Msg) from sender interlocking to receiver interlocking to report the status of an activation zone.	Default		
Eu.ILS.3961	Def	Msg_Approach_Zone_Status	Message (Msg) from sender interlocking to receiver interlocking to report the status of an approach zone.	007000 007400 007800 007900 008000 008200 008400 008800 310900		
Eu.ILS.3964	Def	Msg_Flank_Protection_Status	Message (Msg) from sender interlocking to receiver interlocking to report the status of flank protection.	007000 007400 007800 007900 008400 008800 310900		
Eu.ILS.3962	Def	Msg_Line_Direction_Control	Message (Msg) from sender interlocking to receiver interlocking to report the status of the current line direction, request the line direction "exit" or to hand over the line direction "exit".	007000 007400 007800 007900 008000 008200 008400 008800 310900		
Eu.ILS.3965	Def	Msg_Line_Status	Message (Msg) from sender interlocking to receiver interlocking to report the status of the line.	007000 007400 007800 007900 008000 008200 008400 008800 310900		
Eu.ILS.3966	Def	Msg_Opposite_Main_Signal_Status	Message (Msg) from sender interlocking to receiver interlocking to report the status of the opposite main signals. The opposite main signals are the station main signals which are facing to the line and the boundary indicate the stop aspect. The receiver interlocking is informed from its perspective about the opposite station main signals of the sender interlocking.	007000 007800 007900 008800 310900		
Eu.ILS.3967	Def	Msg_Route_Monitoring_Status	Message (Msg) from sender interlocking to receiver interlocking to report the status of the route monitoring of a secondary route.	007000 007400 007800 007900 008000 008200 008400 008800 310900		
Eu.ILS.3968	Def	Msg_Route_Pretest_Status	Message (Msg) from sender interlocking to receiver interlocking to report the status of a secondary route pretest.	007000 007400		

ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.3969	Def	Msg_Route_Release_Inhibition_Status	Message (Msg) from sender interlocking to receiver interlocking to report the status of the inhibited route release.	007400		
Eu.ILS.3970	Def	Msg_Route_Status	Message (Msg) from sender interlocking to receiver interlocking to report the status of a secondary route.	Default		
Eu.ILS.3971	Def	Msg_Signal_Status	Message (Msg) from sender interlocking to receiver interlocking to report the status of a signal.	007000 007400 007800 007900 008000 008200 008400 008800 310900		
Eu.ILS.3972	Def	Msg_Train_Operated_Route_Release_Status	Message (Msg) from sender interlocking to receiver interlocking to report the status of the train operated release of a secondary route.	Default		
Eu.ILS.3973	Def	Msg_TVPS_Status	Message (Msg) from sender interlocking to receiver interlocking to report the status of a TVPS adjacent to a boundary.	Default		
Eu.ILS.4252	Def	Msg_TDP_Status	Message (Msg) from sender interlocking to receiver interlocking to report the status of a TDP adjacent to a boundary.	008400		
Eu.ILS.1832	Head	3.3 SCI-ILS - Functional Viewpoint		Default		
Eu.ILS.151	Head	3.3.1 SCI-ILS - Functional Context		Default		
Eu.ILS.150	Info	<div><div>[Package] SCI-ILS - Functional Context [Functional Viewpoint - Interface Definition - Initialisation]</div><div><div>uc [Package] SCI-ILS - Functional Context [Functional Viewpoint - Interface Definition - Initialisation]</div><div><p>The diagram illustrates the functional context of the SCI-ILS package. It features a central dashed box labeled 'SCI-ILS' containing three use cases: 'SCI-XX AdjS IFUC1.1: Establish PDI connection' (top), 'SCI-ILS IFUC1.3: Report status' (middle), and 'SCI-XX AdjS IFUC1.2: Close PDI connection' (bottom). An 'include' relationship is shown from the top use case to the middle one. Outside the dashed box, two system boundary blocks are shown: 'Subsystem - Electronic Interlocking' on the left and 'Adjacent Interlocking System' on the right. Lines connect the top and bottom use cases to both system boundaries, and the middle use case is connected to the left boundary.</p></div></div></div>		Default		
Eu.ILS.3887	Info	The generic UseCases SCI-XX AdjS IFUC1.1: Establish PDI connection and SCI-XX AdjS IFUC1.2: Close PDI connection are specified in [Eu.Doc.119].		Default		
Eu.ILS.116	Info	SCI-ILS IFUC1.3: Report status	The Interface-UseCase "SCI-ILS IFUC1.3: Report status" defines the report of the status information which is done for each boundary.	Default		

ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.3833	Info	[Interaction] SCI-ILS IFUC1.3 - Alternative Scenario [SCI-ILS IF SD 1.3.1] <div><div><div><div><div>sd [Interaction] SCI-ILS IFUC1.3 - Alternative Scenario [SCI-ILS IF SD 1.3.1]</div><div><div>:Subsystem - Electronic Interlocking</div><div>:Adjacent Interlocking System</div></div><div><div>Alternative Scenario: Report status</div><div>par<div><div>1.a1 Subsystem - Electronic Interlocking sends the status report to Adjacent Interlocking System.</div><div>also par<div><div>1.b1 Subsystem - Electronic Interlocking sends the status report to Adjacent Interlocking System.</div></div></div></div><div>end par</div></div></div><div><div>par<div><div>ref[Interaction] SCI-ILS IFUC1.3 - Alternative Scenario [SCI-ILS IF SD 1.3.1.1]</div></div><div><div>ref[Interaction] SCI-ILS IFUC1.3 - Alternative Scenario [SCI-ILS IF SD 1.3.1.2]</div></div></div></div></div></div></div></div>	[Interaction] SCI-ILS IFUC1.3 - Alternative Scenario [SCI-ILS IF SD 1.3.1] shows a possible variant of a status report. Which messages are actually sent is dependent from the national requirements and the used or configured functionality in the interlockings.  This SD is part of [SCI-XX AdjS IF SD 1.1.1] in [Eu.Doc.119].	Default		











ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.541	Info	<div><div>[Package] SCI-ILS - Functional Context [Functional Viewpoint - Interface Definition - Operation - Route]</div><div>uc [Package] SCI-ILS - Functional Context [Functional Viewpoint - Interface Definition - Operation - Route]</div><div><p>The diagram shows a dashed box labeled 'SCI-ILS' containing seven green oval use cases: 'SCI-ILS IFUC2.1: Request secondary route', 'SCI-ILS IFUC2.2: Send status of train operated release', 'SCI-ILS IFUC2.3: Cancel secondary route', 'SCI-ILS IFUC2.4: Send status of secondary route monitoring', 'SCI-ILS IFUC2.15: Send status of secondary route', 'SCI-ILS IFUC2.16: Pretest secondary route request', and 'SCI-ILS IFUC2.17: Inhibit route release'. Two 3D cube icons represent external systems: 'Subsystem - Electronic Interlocking' on the left and 'Adjacent Interlocking System' on the right. Lines connect these systems to the use cases: the subsystem to IFUC2.1, IFUC2.3, IFUC2.15, and IFUC2.17; the adjacent system to IFUC2.2, IFUC2.4, IFUC2.16, and IFUC2.17.</p></div></div>		Default		
Eu.ILS.398	Info	SCI-ILS IFUC2.1: Request secondary route	The Interface-UseCase "SCI-ILS IFUC2.1: Request secondary route" defines the request of a secondary route for a boundary.	Default		
Eu.ILS.410	Info	<div><div>[Interaction] SCI-ILS IFUC2.1 - Main Success Scenario [SCI-ILS IF SD 2.1.1]</div><div>sd [Interaction] SCI-ILS IFUC2.1 - Main Success Scenario [SCI-ILS IF SD 2.1.1]</div><div><p>The sequence diagram shows two lifelines: ':Subsystem - Electronic Interlocking' and ':Adjacent Interlocking System'. The process starts with a self-call 'Route_Request' on the subsystem. Then, a message 'Cd_Route_Request' is sent from the subsystem to the adjacent system. Finally, a self-call 'Initiate_Route' occurs on the adjacent system.</p></div><div><p><b>Main Success Scenario: Request secondary route</b></p><p><b>Precondition:</b> The <b>PDI</b> connection is established.</p><p><b>Interaction 2.1.1.A:</b></p><ol style="list-style-type: none"><li>- <b>Subsystem - Electronic Interlocking</b> detects that the secondary route is requested.</li><li><b>Subsystem - Electronic Interlocking</b> sends a secondary route request to <b>Adjacent Interlocking System</b>.</li><li><b>Adjacent Interlocking System</b> initiates the requested secondary route.</li></ol><p><b>Postcondition:</b> The secondary route is initiated by <b>Adjacent Interlocking System</b>.</p></div></div>		Default		
Eu.ILS.420	Info	SCI-ILS IFUC2.2: Send status of train operated release	The Interface-UseCase "SCI-ILS IFUC2.2: Send status of train operated release" defines the report of the train operated release status for a boundary.	Default		



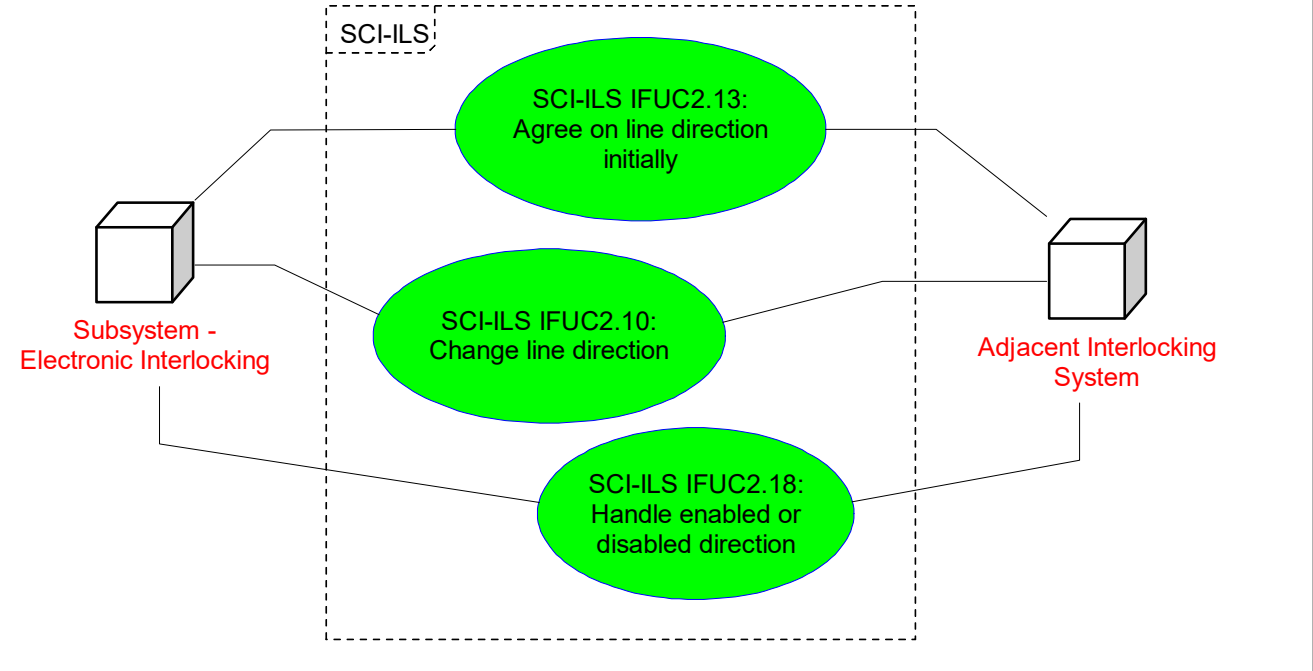
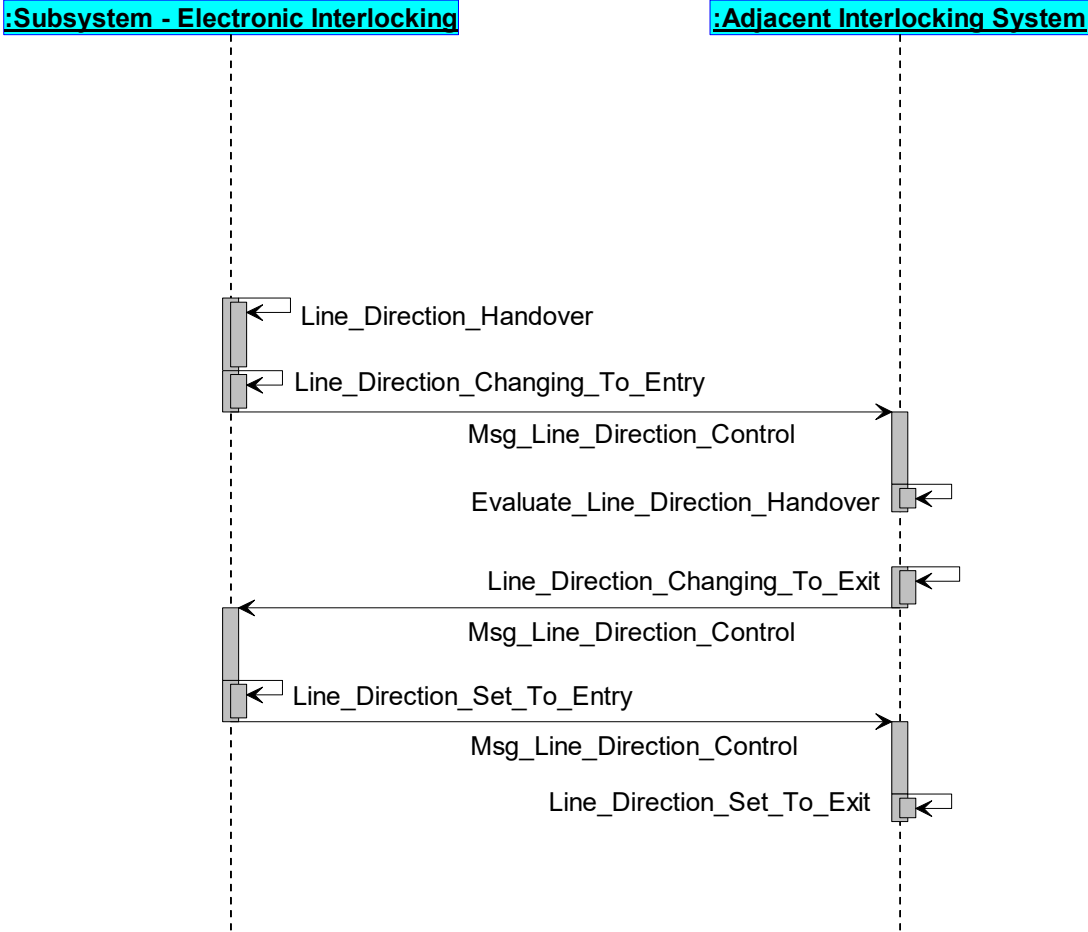




ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.1469	Info	<div><div>[Package] SCI-ILS - Functional Context [Functional Viewpoint - Interface Definition - Operation - Statuses]</div><div><div>uc [Package] SCI-ILS - Functional Context [Functional Viewpoint - Interface Definition - Operation - Statuses]</div><div><div><div>SCI-ILS</div><div><div><div>Subsystem - Electronic Interlocking</div><div>Adjacent Interlocking System</div></div><div><div>SCI-ILS IFUC2.5: Send status of line</div><div>SCI-ILS IFUC2.6: Send status of TVPS</div><div>SCI-ILS IFUC2.7: Send status of approach zone</div><div>SCI-ILS IFUC2.8: Send status of activation zone</div><div>SCI-ILS IFUC2.9: Send status of signal</div><div>SCI-ILS IFUC2.11: Handle flank protection</div><div>SCI-ILS IFUC2.12: Handle access restriction</div><div>SCI-ILS IFUC2.14: Send status of opposite main signals</div><div>SCI-ILS IFUC2.19: Send status of TDP</div></div></div></div></div></div></div>		Default		
Eu.ILS.469	Info	SCI-ILS IFUC2.5: Send status of line	The Interface-UseCase "SCI-ILS IFUC2.5: Send status of line" defines the report of the line status for a boundary. An interlocking reports the status of the line each time it changes.	007000 007400 007800 007900 008000 008200 008400 008800 310900		
Eu.ILS.489	Info	<div><div>[Interaction] SCI-ILS IFUC2.5 - Main Success Scenario [SCI-ILS IF SD 2.5.1]</div><div><div>sd [Interaction] SCI-ILS IFUC2.5 - Main Success Scenario [SCI-ILS IF SD 2.5.1]</div><div><div><div>:Subsystem - Electronic Interlocking</div><div>:Adjacent Interlocking System</div></div><div><div><div><div><div>Own_Line_Status</div><div>Msg_Line_Status</div><div>Adj_Line_Status</div></div></div></div></div></div></div><div><div><div><div><div><div><div>Main Success Scenario: Send status of line</div><div>Precondition: The PDI connection is established.</div><div>Interaction 2.5.1.A: 1. - Subsystem - Electronic Interlocking detects a change of the line status. 2. Subsystem - Electronic Interlocking reports the new status of the line to Adjacent Interlocking System. 3. Adjacent Interlocking System internally responds that the line status of Subsystem - Electronic Interlocking has been updated.</div><div>Postcondition: Subsystem - Electronic Interlocking reported the new status of the line to Adjacent Interlocking System.</div></div></div></div></div></div></div></div>		007000 007400 007800 007900 008000 008200 008400 008800 310900		

ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.501	Info	SCI-ILS IFUC2.6: Send status of TVPS	The Interface-UseCase "SCI-ILS IFUC2.6: Send status of TVPS" defines the report of the TVPS status adjacent to the boundary for one boundary. An interlocking reports the status of the TVPS adjacent to the boundary each time it changes.	Default		
Eu.ILS.502	Info	<div><div>[Interaction] SCI-ILS IFUC2.6 - Main Success Scenario [SCI-ILS IF SD 2.6.1]</div><div><div>sd [Interaction] SCI-ILS IFUC2.6 - Main Success Scenario [SCI-ILS IF SD 2.6.1]</div><div><div><div>:Subsystem - Electronic Interlocking</div><div>:Adjacent Interlocking System</div></div><div><div><div><div></div><div>Own_TVPS_Status</div></div><div><div></div><div>Msg_TVPS_Status</div></div><div><div></div><div>Adj_TVPS_Status</div></div></div></div></div></div><div><b>Main Success Scenario: Send status of TVPS</b> <b>Precondition:</b> The <b>PDI</b> connection is established. <b>Interaction 2.6.1.A:</b> 1. - <b>Subsystem - Electronic Interlocking</b> detects a change of the status of the own TVPS adjacent to the boundary. 2. <b>Subsystem - Electronic Interlocking</b> reports the new status of the TVPS adjacent to the boundary to <b>Adjacent Interlocking System</b>. 3. <b>Adjacent Interlocking System</b> internally responds that the TVPS status of <b>Subsystem - Electronic Interlocking</b> has been updated. <b>Postcondition:</b> <b>Subsystem - Electronic Interlocking</b> reported the new status of the TVPS adjacent to the boundary to <b>Adjacent Interlocking System</b>.</div></div>		Default		
Eu.ILS.511	Info	SCI-ILS IFUC2.7: Send status of approach zone	The Interface-UseCase "SCI-ILS IFUC2.7: Send status of approach zone" defines the report of the approach zone status for a boundary. An interlocking reports the status of an approach zone each time it changes.	007000 007400 007800 007900 008000 008200 008400 008800 310900		
Eu.ILS.512	Info	<div><div>[Interaction] SCI-ILS IFUC2.7 - Main Success Scenario [SCI-ILS IF SD 2.7.1]</div><div><div>sd [Interaction] SCI-ILS IFUC2.7 - Main Success Scenario [SCI-ILS IF SD 2.7.1]</div><div><div><div>:Subsystem - Electronic Interlocking</div><div>:Adjacent Interlocking System</div></div><div><div><div><div></div><div>Own_Approach_Zone_Status</div></div><div><div></div><div>Msg_Approach_Zone_Status</div></div><div><div></div><div>Adj_Approach_Zone_Status</div></div></div></div></div></div><div><b>Main Success Scenario: Send status of approach zone</b> <b>Precondition:</b> The <b>PDI</b> connection is established. <b>Interaction 2.7.1.A:</b> 1. - <b>Subsystem - Electronic Interlocking</b> detects a change of the status of an approach zone. 2. <b>Subsystem - Electronic Interlocking</b> reports the new status of the approach zone to <b>Adjacent Interlocking System</b>. 3. <b>Adjacent Interlocking System</b> internally responds that an approach zone status of <b>Subsystem - Electronic Interlocking</b> has been updated. <b>Postcondition:</b> <b>Subsystem - Electronic Interlocking</b> reported the new status of the approach zone to <b>Adjacent Interlocking System</b>.</div></div>		007000 007400 007800 007900 008000 008200 008400 008800 310900		
Eu.ILS.521	Info	SCI-ILS IFUC2.8: Send status of activation zone	The Interface-UseCase "SCI-ILS IFUC2.8: Send status of activation zone" defines the report of the activation zone status for a boundary. An interlocking reports the status of the activation zone each time it changes.	Default		

ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.522	Info	[Interaction] SCI-ILS IFUC2.8 - Main Success Scenario [SCI-ILS IF SD 2.8.1] <div><div>sd [Interaction] SCI-ILS IFUC2.8 - Main Success Scenario [SCI-ILS IF SD 2.8.1]</div><div><div><div>:Subsystem - Electronic Interlocking</div><div>:Adjacent Interlocking System</div></div><div><div>Main Success Scenario: Send status of activation zone</div><div>Precondition: The PDI connection is established.</div><div>Interaction 2.8.1.A:</div><div>1. - Subsystem - Electronic Interlocking detects the change of the status of an activation zone.</div><div>2. Subsystem - Electronic Interlocking reports the new status of the activation zone to Adjacent Interlocking System.</div><div>3. Adjacent Interlocking System internally responds that an activation zone status of Subsystem - Electronic Interlocking has been updated.</div><div>Postcondition: Subsystem - Electronic Interlocking reported the new status of the activation zone to Adjacent Interlocking System.</div></div></div><pre>sequenceDiagram\n    participant S as :Subsystem - Electronic Interlocking\n    participant A as :Adjacent Interlocking System\n    S-&gt;&gt;S: Own_Activation_Zone_Status\n    S-&gt;&gt;A: Msg_Activation_Zone_Status\n    A--&gt;&gt;A: Adj_Activation_Zone_Status</pre></div>		Default		
Eu.ILS.531	Info	SCI-ILS IFUC2.9: Send status of signal	The Interface-UseCase "SCI-ILS IFUC2.9: Send status of signal" defines the report of the signal status for a boundary. An interlocking reports the status of the signal in advance of the boundary each time it changes.	007000 007400 007800 007900 008000 008200 008400 008800 310900		
Eu.ILS.532	Info	[Interaction] SCI-ILS IFUC2.9 - Main Success Scenario [SCI-ILS IF SD 2.9.1] <div><div>sd [Interaction] SCI-ILS IFUC2.9 - Main Success Scenario [SCI-ILS IF SD 2.9.1]</div><div><div><div>:Subsystem - Electronic Interlocking</div><div>:Adjacent Interlocking System</div></div><div><div>Main Success Scenario: Send status of signal</div><div>Precondition: The PDI connection is established.</div><div>Interaction 2.9.1.A:</div><div>1. - Subsystem - Electronic Interlocking detects a change of the status of the signal in advance of the boundary.</div><div>2. Subsystem - Electronic Interlocking reports the new status of the signal in advance of the boundary to Adjacent Interlocking System.</div><div>3. Adjacent Interlocking System internally responds that the status of the signal in advance of the boundary of Subsystem - Electronic Interlocking has been updated.</div><div>Postcondition: Subsystem - Electronic Interlocking reported the new status of the signal in advance of the boundary to Adjacent Interlocking System.</div></div></div><pre>sequenceDiagram\n    participant S as :Subsystem - Electronic Interlocking\n    participant A as :Adjacent Interlocking System\n    S-&gt;&gt;S: Own_Signal_Status\n    S-&gt;&gt;A: Msg_Signal_Status\n    A--&gt;&gt;A: Adj_Signal_Status</pre></div>		007000 007400 007800 007900 008000 008200 008400 008800 310900		

ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.1468	Info	<div><div>[Package] SCI-ILS - Functional Context [Functional Viewpoint - Interface Definition - Operation - Line Direction]</div><div>uc [Package] SCI-ILS - Functional Context [Functional Viewpoint - Interface Definition - Operation - Line Direction]</div><div></div></div>		Default		
Eu.ILS.152	Info	SCI-ILS IFUC2.10: Change line direction	The Interface-UseCase "SCI-ILS IFUC2.10: Change line direction" defines the change of the direction for a boundary. An interlocking changes its direction dependent on the direction handover or direction request (for the direction "exit") of the adjacent interlocking.	007000 007400 007800 007900 008000 008200 008400 008800 310900		
Eu.ILS.153	Info	<div><div>[Interaction] SCI-ILS IFUC2.10 - Alternative Scenario [SCI-ILS IF SD 2.10.1]</div><div>sd [Interaction] SCI-ILS IFUC2.10 - Alternative Scenario [SCI-ILS IF SD 2.10.1]</div><div><div><div>Alternative Scenario: Handover of line direction "exit"</div><div>Precondition: The PDI connection is established. Subsystem - Electronic Interlocking has the line direction "exit". Adjacent Interlocking System has the line direction "entry".</div><div>Interaction 2.10.1.A: 1. - Subsystem - Electronic Interlocking determines that handover of line direction "exit" to the Adjacent Interlocking System is required. 2. Subsystem - Electronic Interlocking starts changing to entry. 3. Subsystem - Electronic Interlocking reports the handover for line direction "exit" to Adjacent Interlocking System. 4. Adjacent Interlocking System evaluates the line direction handover request.</div><div>Interaction 2.10.1.B: 5. - Adjacent Interlocking System starts to changing to exit. 6. Adjacent Interlocking System reports line direction "exit" to Subsystem - Electronic Interlocking. 7. Subsystem - Electronic Interlocking sets line direction to "entry". 8. Subsystem - Electronic Interlocking reports line direction "entry" to Adjacent Interlocking System. 9. Adjacent Interlocking System sets line direction to "exit".</div><div>Postcondition: Subsystem - Electronic Interlocking has the line direction "entry". Adjacent Interlocking System has the line direction "exit".</div></div><div></div></div></div>	Further explanations regarding the direction synchronisation are described in chapter 3 of [Eu.Doc.10].	007000 007400 007800 007900 008000 008200 008400 008800 310900		



ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.175	Info	[Interaction] SCI-ILS IFUC2.10 - Alternative Scenario [SCI-ILS IF SD 2.10.2] <div><div>sd [Interaction] SCI-ILS IFUC2.10 - Alternative Scenario [SCI-ILS IF SD 2.10.2]</div><div><div><div>:Subsystem - Electronic Interlocking</div><div>:Adjacent Interlocking System</div></div><div><p><b>Alternative Scenario: Request of line direction "exit"</b></p><p><b>Precondition:</b></p><p>The PDI connection is established. Subsystem - Electronic Interlocking is in line direction "exit". Adjacent Interlocking System is in line direction "entry".</p><p><b>Interaction 2.10.2.A:</b></p><p>1. - Adjacent Interlocking System determines that request of line direction "exit" is required.</p><p>2. Adjacent Interlocking System reports the request for line direction "exit" to Subsystem - Electronic Interlocking.</p><p>3. Subsystem - Electronic Interlocking evaluates the request to hand over line direction "exit".</p><p>4. Subsystem - Electronic Interlocking and Adjacent Interlocking System perform the handover of line direction "exit". ...</p><p><b>Postcondition:</b></p><p>Subsystem - Electronic Interlocking is in line direction "entry". Adjacent Interlocking System is in line direction "exit".</p></div><div><div>ref</div><div>[Interaction] SCI-ILS IFUC2.10 - Alternative Scenario [SCI-ILS IF SD 2.10.1]</div></div></div></div>		007800 007900 008000 008400 008800		
Eu.ILS.4167	Info	[Interaction] SCI-ILS IFUC2.10 - Alternative Scenario [SCI-ILS IF SD 2.10.3] <div><div>sd [Interaction] SCI-ILS IFUC2.10 - Alternative Scenario [SCI-ILS IF SD 2.10.3]</div><div><div><div>:Subsystem - Electronic Interlocking</div><div>:Adjacent Interlocking System</div></div><div><p><b>Alternative Scenario: Rejection of handover for line direction "exit"</b></p><p><b>Precondition:</b></p><p>The PDI connection is established. Subsystem - Electronic Interlocking has the line direction "exit". Adjacent Interlocking System has the line direction "entry".</p><p><b>Interaction 2.10.3.A:</b></p><p>1. - Subsystem - Electronic Interlocking determines that handover of line direction "exit" to the Adjacent Interlocking System is required.</p><p>2. Subsystem - Electronic Interlocking starts changing to entry.</p><p>3. Subsystem - Electronic Interlocking reports the handover for line direction "exit" to Adjacent Interlocking System.</p><p>4. Adjacent Interlocking System evaluates the line direction handover request.</p><p><b>Interaction 2.10.3.B:</b></p><p>5. - Adjacent Interlocking System detects that handover of line direction "exit" is rejected.</p><p>6. Adjacent Interlocking System reports to Subsystem - Electronic Interlocking that the handover for line direction "exit" is rejected.</p><p>7. Subsystem - Electronic Interlocking processes the rejection of the line direction handover and keeps line direction 'exit'.</p><p><b>Postcondition:</b></p><p>---</p></div></div></div>	Further explanations regarding the direction synchronisation are described in chapter 3 of [Eu.Doc.10].	008200		













ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.287	Info	[Interaction] SCI-ILS IFUC2.13 - Alternative Scenario [SCI-ILS IF SD 2.13.4] <div><div>sd [Interaction] SCI-ILS IFUC2.13 - Alternative Scenario [SCI-ILS IF SD 2.13.4]</div><div><div>:Subsystem - Electronic Interlocking</div><div>:Adjacent Interlocking System</div><div><p><b>Alternative Scenario: "exit", "exit"</b></p><p><b>Precondition:</b></p><p>The PDI connection is established. The own last known direction of Subsystem - Electronic Interlocking is "exit". The own last known direction of Adjacent Interlocking System is "exit".</p><p><b>par</b></p><div><p><b>Interaction 2.13.4.A:</b></p><p><b>1.a1</b> - Subsystem - Electronic Interlocking evaluates that Adjacent Interlocking System has sent the direction "exit" at initialisation.</p><p><b>1.a2</b> Subsystem - Electronic Interlocking has the same direction as Adjacent Interlocking System.</p></div><p><b>also par</b></p><div><p><b>Interaction 2.13.4.B:</b></p><p><b>1.b1</b> - Adjacent Interlocking System evaluates that Subsystem - Electronic Interlocking has sent the direction "exit" at initialisation.</p><p><b>1.a2</b> Adjacent Interlocking System has the same direction as Subsystem - Electronic Interlocking.</p></div></div><p><b>end par</b></p><p><b>Interaction 2.13.4.C</b></p><p><b>2.</b> - Subsystem - Electronic Interlocking detects that direction reset is initiated.</p><p><b>3.</b> Subsystem - Electronic Interlocking starts changing to direction "entry".</p><p><b>Interaction 2.13.4.D:</b></p><p><b>4.</b> - Subsystem - Electronic Interlocking detects that its direction changed to "entry".</p><p><b>5.</b> Subsystem - Electronic Interlocking reports to Adjacent Interlocking System that it is in direction "entry".</p><p><b>6.</b> Adjacent Interlocking System synchronises in the direction "exit".</p><p><b>Postcondition:</b></p><p>Subsystem - Electronic Interlocking is synchronised in direction "entry". Adjacent Interlocking System is synchronised in direction "exit".</p></div></div>	The occurrence of direction "exit" in both interlockings is a safety-relevant situation.	007000 007400 007800 007900 008000 008200 008400 008800 310900		
Eu.ILS.334	Info	[Interaction] SCI-ILS IFUC2.13 - Alternative Scenario [SCI-ILS IF SD 2.13.5] <div><div>sd [Interaction] SCI-ILS IFUC2.13 - Alternative Scenario [SCI-ILS IF SD 2.13.5]</div><div><div>:Subsystem - Electronic Interlocking</div><div>:Adjacent Interlocking System</div><div><p><b>Alternative Scenario: "no direction", "exit"</b></p><p><b>Precondition:</b></p><p>The PDI connection is established. The own last known direction of Subsystem - Electronic Interlocking is "no direction". The own last known direction of Adjacent Interlocking System is "exit".</p><p><b>par</b></p><div><p><b>Interaction 2.13.5.A:</b></p><p><b>1.a1</b> - Subsystem - Electronic Interlocking evaluates that Adjacent Interlocking System has sent the direction "exit" at initialisation.</p><p><b>1.a2</b> Subsystem - Electronic Interlocking starts changing to direction "entry".</p></div><p><b>also par</b></p><div><p><b>Interaction 2.13.5.B:</b></p><p><b>1.b1</b> - Adjacent Interlocking System evaluates that Subsystem - Electronic Interlocking has sent the direction "no direction" at initialisation.</p><p><b>1.b2</b> Adjacent Interlocking System is in direction "exit" and not synchronised.</p></div></div><p><b>end par</b></p><p><b>Interaction 2.13.5.C:</b></p><p><b>2.</b> - Subsystem - Electronic Interlocking detects that its direction changed to "entry".</p><p><b>3.</b> Subsystem - Electronic Interlocking reports to Adjacent Interlocking System that it is in direction "entry".</p><p><b>4.</b> Adjacent Interlocking System synchronises in the direction "exit".</p><p><b>Postcondition:</b></p><p>Subsystem - Electronic Interlocking is synchronised in direction "entry". Adjacent Interlocking System is synchronised in direction "exit".</p></div></div>		007000 007400 007800 007900 008000 008200 008400 008800 310900		



ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.346	Info	[Interaction] SCI-ILS IFUC2.13 - Alternative Scenario [SCI-ILS IF SD 2.13.6] <div><div>sd [Interaction] SCI-ILS IFUC2.13 - Alternative Scenario [SCI-ILS IF SD 2.13.6]</div><div><div>:Subsystem - Electronic Interlocking</div><div>:Adjacent Interlocking System</div><div><p><b>Alternative Scenario:</b> "no direction", "no direction"</p><p><b>Precondition:</b></p><p>The PDI connection is established. The own last known direction of Subsystem - Electronic Interlocking is "no direction". The own last known direction of Adjacent Interlocking System is "no direction". The own configured direction of Subsystem - Electronic Interlocking is "entry". The own configured direction of Adjacent Interlocking System is "exit".</p><p><b>par</b></p><div><p><b>Interaction 2.13.6.A:</b></p><p>1.a1 - Subsystem - Electronic Interlocking evaluates that Adjacent Interlocking System has sent "no direction" at initialisation.</p><p>1.a2 Subsystem - Electronic Interlocking is configured in direction "entry" and starts changing to direction "entry".</p></div><p><b>also par</b></p><div><p><b>Interaction 2.13.6.B:</b></p><p>1.b1 - Adjacent Interlocking System evaluates that Subsystem - Electronic Interlocking has sent "no direction" at initialisation.</p><p>1.b2 Adjacent Interlocking System is configured in direction "exit" and starts waiting for Subsystem - Electronic Interlocking until it reports direction "entry".</p></div></div><p><b>end par</b></p><p><b>Interaction 2.13.6.C:</b></p><p>2. - Subsystem - Electronic Interlocking detects that its direction changed to "entry".</p><p>3. Subsystem - Electronic Interlocking reports to Adjacent Interlocking System that it is in direction "entry".</p><p>4. Adjacent Interlocking System starts changing to direction "exit".</p><p><b>Interaction 2.13.6.D:</b></p><p>5. - Adjacent Interlocking System detects that its direction changed to "exit".</p><p>6. Adjacent Interlocking System reports to Subsystem - Electronic Interlocking that it is in direction "exit".</p><p><b>Postcondition:</b></p><p>Subsystem - Electronic Interlocking is synchronised in direction "entry". Adjacent Interlocking System is synchronised in direction "exit".</p></div></div>		007000 007400 007800 007900 008000 008200 008400 008800 310900		
Eu.ILS.1531	Info	SCI-ILS IFUC2.14: Send status of opposite main signals	The Interface-UseCase "SCI-ILS IFUC2.14: Send status of opposite main signals" defines the report of the opposite station main signal status for a boundary.	007800 007900 008800 310900		
Eu.ILS.1532	Info	[Interaction] SCI-ILS IFUC2.14 - Main Success Scenario [SCI-ILS IF SD 2.14.1] <div><div>sd [Interaction] SCI-ILS IFUC2.14 - Main Success Scenario [SCI-ILS IF SD 2.14.1]</div><div><div>:Subsystem - Electronic Interlocking</div><div>:Adjacent Interlocking System</div><div><p><b>Main Success Scenario:</b> Send status of opposite main signals</p><p><b>Precondition:</b></p><p>The PDI connection is established.</p><p><b>Interaction 2.14.1.A:</b></p><p>1. - Subsystem - Electronic Interlocking detects that all station main signals which are facing to the line and the boundary indicate the stop aspect.</p><p>2. Subsystem - Electronic Interlocking reports the new status of the opposite station main signals to Adjacent Interlocking System.</p><p>3. Adjacent Interlocking System internally responds that the opposite main signal status of Subsystem - Electronic Interlocking has been updated.</p><p><b>Postcondition:</b></p><p>Subsystem - Electronic Interlocking reported the new status of the opposite main signals to Adjacent Interlocking System.</p></div></div></div>		007800 007900 008800 310900		



ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.1810	Info	<div><div>[Interaction] SCI-ILS IFUC2.17 - Main Success Scenario [SCI-ILS IF SD 2.17.1]</div><div><div>sd [Interaction] SCI-ILS IFUC2.17 - Main Success Scenario [SCI-ILS IF SD 2.17.1]</div><div><div><div>:Subsystem - Electronic Interlocking</div><div>:Adjacent Interlocking System</div></div><div><div><div><div></div><div>Route_Release_Inhibition_Activation_Request</div><div></div></div><div><div></div><div>Cd_Route_Release_Inhibition_Activation_Request</div><div></div></div><div><div></div><div>Initiate_Route_Release_Inhibition</div><div></div></div><div><div></div><div>Own_Route_Release_Inhibition_Status</div><div></div></div><div><div></div><div>Msg_Route_Release_Inhibition_Status</div><div></div></div><div><div></div><div>Adj_Route_Release_Inhibition_Status</div><div></div></div></div></div></div><div><p><b>Main Success Scenario: Activate inhibited route release</b></p><p><b>Precondition:</b></p><p>The <b>PDI</b> connection is established.</p><p><b>Interaction 2.17.1.A:</b></p><p>1. - <b>Subsystem - Electronic Interlocking</b> detects that the inhibited route release is requested.</p><p>2. <b>Subsystem - Electronic Interlocking</b> sends a inhibited route release request to <b>Adjacent Interlocking System</b>.</p><p>3. <b>Adjacent Interlocking System</b> initiates the inhibited route release.</p><p><b>Interaction 2.17.1.B:</b></p><p>4. - <b>Adjacent Interlocking System</b> detects that the inhibited route release is activated.</p><p>5. <b>Adjacent Interlocking System</b> reports to <b>Subsystem - Electronic Interlocking</b> that the inhibited route release is activated.</p><p>6. <b>Subsystem - Electronic Interlocking</b> internally responds that the inhibited route release status of <b>Adjacent Interlocking System</b> has been updated and the inhibited route release is activated.</p><p><b>Postcondition:</b></p><p><b>Adjacent Interlocking System</b> reported the new status of the activated inhibited route release to <b>Subsystem - Electronic Interlocking</b>.</p></div></div></div>		007400		
Eu.ILS.3835	Info	SCI-ILS IFUC2.18: Handle enabled or disabled direction	The Interface-UseCase "SCI-ILS IFUC2.18: Handle enabled or disabled direction" defines the function "Enable/disable direction" (Dutch: Uitschakelen / herstellen linkerspoor) and is used for a line block with 'manual direction control'. It is a degrade mode function. In case of a disturbance in the line block, for example a disturbance in a track circuit, the function "disable direction" is used to disable the current not regular direction, mostly the left track of a double track line. If the line block is disable no routes can be set to the line block from both sites and the activation zones of level crossings are set for both directions. A command to disable or enable the direction of one track can only requested by one interlocking.	008400 310900		



ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.3836	Info	<div><div>[Interaction] SCI-ILS IFUC2.18 - Alternative Scenario [SCI-ILS IF SD 2.18.1]</div><div><div>sd [Interaction] SCI-ILS IFUC2.18 - Alternative Scenario [SCI-ILS IF SD 2.18.1]</div><div><div><div>:Subsystem - Electronic Interlocking</div><div>:Adjacent Interlocking System</div></div><div><div><div>Alternative Scenario: Enable direction</div><div>Precondition: The PDI connection is established.</div><div>Interaction 2.18.1.A:</div><div>1. - Subsystem - Electronic Interlocking detects that it is requested to enable the line block direction.</div><div>2. Subsystem - Electronic Interlocking sends a message to Adjacent Interlocking System to enable the direction of the line block.</div><div>3. Adjacent Interlocking System enables the line block direction.</div><div>Interaction 2.18.1.B:</div><div>4. - Adjacent Interlocking System detects that the line block direction status changed.</div><div>5. Adjacent Interlocking System reports the line block direction status to Subsystem - Electronic Interlocking.</div><div>6. Subsystem - Electronic Interlocking internally responds that the line block direction status of Adjacent Interlocking System changed.</div><div>Postcondition: The line block direction is no longer disabled.</div></div></div><div><div>Enable_Line_Block_Direction_Request</div><div>Msg_Line_Direction_Control</div><div>Enable_Line_Block_Direction</div><div>Own_Line_Block_Direction_Status</div><div>Msg_Line_Direction_Control</div><div>Adj_Line_Block_Direction_Status</div></div></div></div></div>		008400 310900		
Eu.ILS.3837	Info	<div><div>[Interaction] SCI-ILS IFUC2.18 - Alternative Scenario [SCI-ILS IF SD 2.18.2]</div><div><div>sd [Interaction] SCI-ILS IFUC2.18 - Alternative Scenario [SCI-ILS IF SD 2.18.2]</div><div><div><div>:Subsystem - Electronic Interlocking</div><div>:Adjacent Interlocking System</div></div><div><div><div>Alternative Scenario: Disable direction</div><div>Precondition: The PDI connection is established.</div><div>Interaction 2.18.2.A:</div><div>1. - Subsystem - Electronic Interlocking detects that it is requested to disable the line block direction.</div><div>2. Subsystem - Electronic Interlocking sends a message to Adjacent Interlocking System to disable the direction of the line block.</div><div>3. Adjacent Interlocking System disables the line block direction.</div><div>Interaction 2.18.2.B:</div><div>4. - Adjacent Interlocking System detects that the line block direction status changed.</div><div>5. Adjacent Interlocking System reports the line block direction status to Subsystem - Electronic Interlocking.</div><div>6. Subsystem - Electronic Interlocking internally responds that the line block direction status of Adjacent Interlocking System changed.</div><div>Postcondition: The line block direction is disabled.</div></div></div><div><div>Disable_Line_Block_Direction_Request</div><div>Msg_Line_Direction_Control</div><div>Disable_Line_Block_Direction</div><div>Own_Line_Block_Direction_Status</div><div>Msg_Line_Direction_Control</div><div>Adj_Line_Block_Direction_Status</div></div></div></div></div>		008400 310900		
Eu.ILS.4250	Info	SCI-ILS IFUC2.19: Send status of TDP	The Interface-UseCase "SCI-ILS IFUC2.19: Send status of TDP" defines the report of the TDP status related to the boundary for one boundary. An interlocking reports the status of the TDP related to the boundary each time it changes.	008400		



ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.1835	Def	<div><div>[Block] SCI-ILS [Functional Viewpoint - Interface Requirements - Functional Architecture 1]</div><div><div>ibd [Block] SCI-ILS [Functional Viewpoint - Interface Requirements - Functional Architecture 1]</div><div><div><div>«logical structural entity» SCI-ILS</div><div><div><div><div>«participant» {end = SCI-ILS} «logical structural entity» InLink : Subsystem - Electronic Interlocking</div><div><div><div>«functional entity» SCI-ILS EIL generic : S_SCI_Adj_Sec P1inout : PDI_GEN_ADJ</div><div><div>t27out_Check_Prim_Status t25in_Prim_Status_Report_Complete</div><div>p2inout : ~SCI_AdjS_Sec_Specific</div><div>p2inout : SCI_AdjS_Sec_Specific</div><div><div>t25out_Adj_Status_Report_Complete t27in_Check_Adj_Status</div><div>«functional entity» SCI-ILS EIL : S_SCI_ILS D6in_Con_Primary P101inout : SCI_ILS</div></div></div><div><div>«participant» {end = SCI-ILS} «environmental structural entity» InLink : Adjacent Interlocking System</div><div><div><div>«functional entity» SCI-ILS AIS generic : S_SCI_Adj_Prim P1inout : ~PDI_GEN_ADJ</div><div><div>t27out_Check_Sec_Status t25in_Sec_Status_Report_Complete</div><div>p2inout : ~SCI_AdjS_Prim_Specific</div><div>p2inout : SCI_AdjS_Prim_Specific</div><div><div>t25out_Adj_Status_Report_Complete t27in_Check_Adj_Status</div><div>«functional entity» SCI-ILS AIS : S_SCI_ILS D6in_Con_Primary P101inout : SCI_ILS</div></div></div></div></div><div><div>EIL1 : SCI_ILS_EIL AIS1 : SCI_ILS_AIS</div><div><div>P1inout : PDI_GEN_ADJ P1inout : ~PDI_GEN_ADJ</div><div><div>P101inout : SCI_ILS P101inout : SCI_ILS</div></div></div><div><div>«equal»</div><div>«equal»</div><div>«equal»</div><div>«equal»</div></div></div></div></div></div></div></div></div></div></div></div></div>	<div>[Block] SCI-ILS [Functional Viewpoint - Interface Requirements - Functional Architecture 1] defines the functional architecture of the SCI-ILS PDI for one boundary. This diagram shows the exchanges between the generic and specific functional entities.</div>	Default		

ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.4166	Def	<div><div>[Block] SCI-ILS [Functional Viewpoint - Interface Requirements - Functional Architecture 2]</div><div><div>ibd [Block] SCI-ILS [Functional Viewpoint - Interface Requirements - Functional Architecture 2]</div><div><div>«logical structural entity» SCI-ILS</div><div><div><div><div>«participant» {end = SCI-ILS} «logical structural entity» InLink : Subsystem - Electronic Interlocking</div><div><div><div><div>D1000in_Con_007600</div><div>D1002in_Con_008000</div><div>D1004in_Con_008400</div><div>D1006in_Con_310900</div><div>D1008in_Con_007000</div><div>«functional entity» SCI-ILS EIL : S_SCI_ILS D1005in_Con_008800</div><div>t8out_Start_Status_Report</div><div>t5in_Status_Report_Completed</div><div>t6out_Check_Adj_Status</div><div>t7in_Adj_Status_Report_Complete</div><div>p3inout : SCI_ILS_Internal</div><div>p3inout : ~SCI_ILS_Internal</div><div>t7out_Adj_Status_Report_Complete</div><div>t6in_Check_Adj_Status</div><div>«functional entity» SCI-ILS EIL National : S_SCI_ILS_National</div><div>t5out_Status_Report_Completed</div><div>t8in_Start_Status_Report</div></div></div><div><div>EIL1 : SCI_ILS_EIL AIS1 : SCI_ILS_AIS</div><div>P101inout : SCI_ILS</div><div>«equal»</div><div>P101inout : SCI_ILS</div><div>«equal»</div></div><div><div><div><div>D1000in_Con_007600</div><div>D1002in_Con_008000</div><div>D1004in_Con_008400</div><div>D1006in_Con_310900</div><div>D1008in_Con_007000</div><div>«functional entity» SCI-ILS AIS : S_SCI_ILS D1005in_Con_008800</div><div>D1007in_Con_007400</div><div>t8out_Start_Status_Report</div><div>t5in_Status_Report_Completed</div><div>t6out_Check_Adj_Status</div><div>t7in_Adj_Status_Report_Complete</div><div>p3inout : SCI_ILS_Internal</div><div>p3inout : ~SCI_ILS_Internal</div><div>t7out_Adj_Status_Report_Complete</div><div>t6in_Check_Adj_Status</div><div>«functional entity» SCI-ILS AIS National : S_SCI_ILS_National</div><div>t5out_Status_Report_Completed</div><div>t8in_Start_Status_Report</div></div></div></div></div></div></div></div></div></div></div>	<div>[Block] SCI-ILS [Functional Viewpoint - Interface Requirements - Functional Architecture 2] defines the functional architecture of the SCI-ILS PDI for one boundary. This diagram shows the exchanges between the specific functional entities.</div>	Default		
Eu.ILS.1830	Info	<div>The defined model elements represent the interface SCI-ILS in a general way. This refers to:  - The specific part of the SCI-ILS PDI represented by functional entity instances of S_SCI_ILS in [Block] SCI-ILS [Functional Viewpoint - Interface Requirements - Functional Architecture 1], describes the communication for one boundary. - The generic part of the SCI-ILS PDI represented by functional entity instances of S_SCI_Adj_Prim and S_SCI_Adj_Sec in [Block] SCI-ILS [Functional Viewpoint - Interface Requirements - Functional Architecture 1], describes the communication for all boundaries.</div>		Default		
Eu.ILS.4149	Head	3.3.4 SCI-ILS - Functional Entities		Default		
Eu.ILS.3353	Info	S_SCI_ILS	<div>The functional entity S_SCI_ILS represents the EULYNX specific part of the interface. It complements the functional entity S_SCI_ILS_National. An instance of S_SCI_ILS represents one boundary.</div>	-		

ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.3642	Req	<div>[Block] S_SCI_ILS [Functional Viewpoint - Interface Requirements - Functional Entity]</div> <div><div>ibd [Block] S_SCI_ILS [Functional Viewpoint - Interface Requirements - Functional Entity]</div><div><div><div>«functional entity» S_SCI_ILS</div><div><div><div>P101inout : SCI_ILS</div><div>D6in_Con_Primary : Boolean</div><div>D1000in_Con_007600 : Boolean</div><div>D1001in_Con_007900 : Boolean</div><div>D1002in_Con_008000 : Boolean</div><div>D1003in_Con_008200 : Boolean</div><div>D1004in_Con_008400 : Boolean</div><div>D1005in_Con_008800 : Boolean</div><div>D1006in_Con_310900 : Boolean</div><div>D1007in_Con_007400 : Boolean</div><div>D1008in_Con_007000 : Boolean</div></div><div><div>p3inout : SCI_ILS_Internal</div><div>p2inout : SCI_AdjS_Prim_Specific</div><div>p2inout : SCI_AdjS_Sec_Specific</div><div>t8out_Start_Status_Report : PulsedOut</div><div>t5in_Status_Report_Completed : PulsedIn</div><div>t27in_Check_Adj_Status : PulsedIn</div><div>t25out_Adj_Status_Report_Complete : PulsedOut</div><div>t6out_Check_Adj_Status : PulsedOut</div><div>t7in_Adj_Status_Report_Complete : PulsedIn</div></div></div></div></div></div>		-		
Eu.ILS.3355	Def	D1000in_Con_007600	The port D1000in_Con_007600 provides the configuration value whether an interlocking is configured for the infrastructure manager 007600.	-		
Eu.ILS.3356	Def	D1001in_Con_007900	The port D1001in_Con_007900 provides the configuration value whether an interlocking is configured for the infrastructure manager 007900.	-		
Eu.ILS.3357	Def	D1002in_Con_008000	The port D1002in_Con_008000 provides the configuration value whether an interlocking is configured for the infrastructure manager 008000.	-		
Eu.ILS.3358	Def	D1003in_Con_008200	The port D1003in_Con_008200 provides the configuration value whether an interlocking is configured for the infrastructure manager 008200.	-		
Eu.ILS.3359	Def	D1004in_Con_008400	The port D1004in_Con_008400 provides the configuration value whether an interlocking is configured for the infrastructure manager 008400.	-		
Eu.ILS.3360	Def	D1005in_Con_008800	The port D1005in_Con_008800 provides the configuration value whether an interlocking is configured for the infrastructure manager 008800.	-		
Eu.ILS.3361	Def	D1006in_Con_310900	The port D1006in_Con_310900 provides the configuration value whether an interlocking is configured for the infrastructure manager 310900.	-		

ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.3362	Def	D1007in_Con_007400	The port D1007in_Con_007400 provides the configuration value whether an interlocking is configured for the infrastructure manager 007400.	-		
Eu.ILS.4168	Def	D1008in_Con_007000	The port D1008in_Con_007000 provides the configuration value whether an interlocking is configured for the infrastructure manager 007000.	-		
Eu.ILS.3367	Def	D6in_Con_Primary	The port D6in_Con_Primary provides the configuration value whether an interlocking is the primary communication partner.  - True: primary communication partner - False: secondary communication partner	-		
Eu.ILS.4043	Def	P101inout	The port P101inout exchanges information objects according to SCI_ILS.	-		
Eu.ILS.4144	Def	p2inout		-		
Eu.ILS.4145	Def	p2inout		-		
Eu.ILS.4044	Def	p3inout		-		
Eu.ILS.3648	Def	t5in_Status_Report_Completed		-		
Eu.ILS.3878	Def	t27in_Check_Adj_Status		-		
Eu.ILS.3879	Def	t25out_Adj_Status_Report_Complete		-		
Eu.ILS.3880	Def	t6out_Check_Adj_Status		-		
Eu.ILS.3881	Def	t7in_Adj_Status_Report_Complete		-		
Eu.ILS.3892	Def	t8out_Start_Status_Report		-		
Eu.ILS.3378	Info	S_SCI_ILS - Behaviour		-		

ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.3638	Req	<div>Functional Viewpoint - Interface Requirements - Functional Entity STD 1</div> <div>stm [State Machine] S_SCI_ILS - Behaviour [Functional Viewpoint - Interface Requirements - Functional Entity STD 1]</div> <div></div>	<p>This state machine diagram describes the requirements for the following functionalities:</p> <ul style="list-style-type: none"><li>- definition of the PDI connection states</li><li>- changes between the PDI connection states and the conditions for these changes</li></ul>	-		
Eu.ILS.3450	Def	Initial0		-		
Eu.ILS.3451	Def	/ {Initial0 - PDI_CONNECTION_CLOSED}		-		
Eu.ILS.3455	Def	PDI_CONNECTION_CLOSED		-		
Eu.ILS.3456	Def	Establishing_PDI_Connection / {PDI_CONNECTION_CLOSED - ESTABLISHING_PDI_CONNECTION}		-		
Eu.ILS.3847	Def	ESTABLISHING_PDI_CONNECTION		-		
Eu.ILS.3849	Def	Initial1		-		



ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.3850	Def	/{Initial1 - Junction0}		-		
Eu.ILS.3851	Def	Junction0		-		
Eu.ILS.3852	Def	[D6in_Con_Primary]/ {Junction0 - RECEIVING_STATUS}		-		
Eu.ILS.3853	Def	[NOT D6in_Con_Primary]/ {Junction0 - SEC_WAITING}		-		
Eu.ILS.3854	Def	CHECKING_STATUS		-		
Eu.ILS.3855	Def	when(t7in_Adj_Status_Report_Complete)/ t25out_Adj_Status_Report_Complete := TRUE; {CHECKING_STATUS - Junction1}		-		
Eu.ILS.4156	Def	Junction1		-		
Eu.ILS.4157	Def	[D6in_Con_Primary]/ {Junction1 - PRIM_WAITING}		-		
Eu.ILS.3865	Def	[NOT D6in_Con_Primary]/ {Junction1 - STATUS_REPORT_COMPLETED}		-		
Eu.ILS.3856	Def	STATUS_REPORT_COMPLETED		-		
Eu.ILS.3641	Def	PDI_Connection_Established/ {STATUS_REPORT_COMPLETED - Final0}		-		
Eu.ILS.3862	Def	PRIM_WAITING		-		
Eu.ILS.3863	Def	Start_Prim_Status_Report/ {PRIM_WAITING - SENDING_STATUS}		-		
Eu.ILS.3873	Def	SEC_WAITING		-		
Eu.ILS.3874	Def	Start_Sec_Status_Report/ t8out_Start_Status_Report := TRUE; {SEC_WAITING - SENDING_STATUS}		-		
Eu.ILS.3449	Def	PDI_Connection_Closed/ {ESTABLISHING_PDI_CONNECTION - PDI_CONNECTION_CLOSED}		-		
Eu.ILS.3417	Def	Final0		-		
Eu.ILS.3848	Def	/{ESTABLISHING_PDI_CONNECTION - PDI_CONNECTION_ESTABLISHED}		-		
Eu.ILS.4158	Def	Junction2		-		
Eu.ILS.3448	Def	[NOT D6in_Con_Primary]/ send Sec_Status_Report_Completed to p2inout; {Junction2 - RECEIVING_STATUS}		-		
Eu.ILS.3415	Def	[D6in_Con_Primary]/ send Prim_Status_Report_Completed to p2inout; {Junction2 - STATUS_REPORT_COMPLETED}		-		
Eu.ILS.4159	Def	RECEIVING_STATUS		-		
Eu.ILS.4160	Req	<div>Functional Viewpoint - Interface Requirements - Functional Entity STD 1.1</div> <div><div>strn [Atomic State] RECEIVING_STATUS [Functional Viewpoint - Interface Requirements - Functional Entity STD 1.1]</div><div><div><div>●</div><div>Initial0</div><div>↓</div></div><div><div>RECEIVING_STATUS_MESSAGES</div><div>Msg_Access_Restriction_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1006in_Con_310900 OR D1008in_Con_007000] / send Internal_Msg_Access_Restriction_Status to p3inout; Msg_Activation_Zone_Status/send Internal_Msg_Activation_Zone_Status to p3inout; Msg_Approach_Zone_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000] / send Internal_Msg_Approach_Zone_Status to p3inout; Msg_Flank_Protection_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000] / send Internal_Msg_Flank_Protection_Status to p3inout; Msg_Line_Direction_Control[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000] / send Internal_Msg_Line_Direction_Control to p3inout; Msg_Line_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000] / send Internal_Msg_Line_Status to p3inout; Msg_Opposite_Main_Signal_Status[D1001in_Con_007900 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000] / send Internal_Msg_Opposite_Main_Signal_Status to p3inout; Msg_Route_Monitoring_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000] / send Internal_Msg_Route_Monitoring_Status to p3inout; Msg_Route_Status/send Internal_Msg_Route_Status to p3inout; Msg_Signal_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000] / send Internal_Msg_Signal_Status to p3inout; Msg_TVPS_Status/send Internal_Msg_TVPS_Status to p3inout; Msg_TDP_Status[D1004in_Con_008400] / send Internal_Msg_TDP_Status to p3inout;</div></div></div></div>	<div>This state machine diagram describes the requirements for the following functionalities:</div> <div>- receive status messages from the communication partner during initial PDI status report and forward them to the internal logic</div>	-		



ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.4067	Def	Initial0		-		
Eu.ILS.4068	Def	/{Initial0 - RECEIVING_STATUS_MESSAGES}		-		
Eu.ILS.4069	Def	RECEIVING_STATUS_MESSAGES		-		
Eu.ILS.4119	Def	Msg_Access_Restriction_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Msg_Access_Restriction_Status to p3inout;{State-internal in RECEIVING_STATUS_MESSAGES}		-		
Eu.ILS.4123	Def	Msg_Activation_Zone_Status/send Internal_Msg_Activation_Zone_Status to p3inout;{State-internal in RECEIVING_STATUS_MESSAGES}		-		
Eu.ILS.4124	Def	Msg_Approach_Zone_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Msg_Approach_Zone_Status to p3inout;{State-internal in RECEIVING_STATUS_MESSAGES}		-		
Eu.ILS.4126	Def	Msg_Flank_Protection_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Msg_Flank_Protection_Status to p3inout;{State-internal in RECEIVING_STATUS_MESSAGES}		-		
Eu.ILS.4127	Def	Msg_Line_Direction_Control[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Msg_Line_Direction_Control to p3inout;{State-internal in RECEIVING_STATUS_MESSAGES}		-		
Eu.ILS.4128	Def	Msg_Line_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Msg_Line_Status to p3inout;{State-internal in RECEIVING_STATUS_MESSAGES}		-		
Eu.ILS.4129	Def	Msg_Opposite_Main_Signal_Status[D1001in_Con_007900 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Msg_Opposite_Main_Signal_Status to p3inout;{State-internal in RECEIVING_STATUS_MESSAGES}		-		
Eu.ILS.4130	Def	Msg_Route_Monitoring_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Msg_Route_Monitoring_Status to p3inout;{State-internal in RECEIVING_STATUS_MESSAGES}		-		
Eu.ILS.4120	Def	Msg_Route_Status/send Internal_Msg_Route_Status to p3inout;{State-internal in RECEIVING_STATUS_MESSAGES}		-		
Eu.ILS.4121	Def	Msg_Signal_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Msg_Signal_Status to p3inout;{State-internal in RECEIVING_STATUS_MESSAGES}		-		
Eu.ILS.4122	Def	Msg_TVPS_Status/send Internal_Msg_TVPS_Status to p3inout;{State-internal in RECEIVING_STATUS_MESSAGES}		-		
Eu.ILS.4260	Def	Msg_TDP_Status[D1004in_Con_008400]/send Internal_Msg_TDP_Status to p3inout;{State-internal in RECEIVING_STATUS_MESSAGES}		-		
Eu.ILS.3859	Def	when(t27in_Check_Adj_Status)/ t6out_Check_Adj_Status := TRUE;{RECEIVING_STATUS - CHECKING_STATUS}		-		
Eu.ILS.4161	Def	SENDING_STATUS		-		
Eu.ILS.4162	Req	<div>Functional Viewpoint - Interface Requirements - Functional Entity STD 1.2</div> <div><div>stm [Atomic State] SENDING_STATUS [Functional Viewpoint - Interface Requirements - Functional Entity STD 1.2]</div><div><div><div>Initial0</div><div>SENDING_STATUS_MESSAGES</div><div>Internal_Msg_Access_Restriction_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1006in_Con_310900 OR D1008in_Con_007000] / send Msg_Access_Restriction_Status to P101inout; Internal_Msg_Activation_Zone_Status/send Msg_Activation_Zone_Status to P101inout; Internal_Msg_Approach_Zone_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000] / send Msg_Approach_Zone_Status to P101inout; Internal_Msg_Flank_Protection_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000] / send Msg_Flank_Protection_Status to P101inout; Internal_Msg_Line_Direction_Control[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000] / send Msg_Line_Direction_Control to P101inout; Internal_Msg_Line_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000] / send Msg_Line_Status to P101inout; Internal_Msg_Opposite_Main_Signal_Status[D1001in_Con_007900 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000] / send Msg_Opposite_Main_Signal_Status to P101inout; Internal_Msg_Route_Monitoring_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000] / send Msg_Route_Monitoring_Status to P101inout; Internal_Msg_Route_Status/send Msg_Route_Status to P101inout; Internal_Msg_Signal_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000] / send Msg_Signal_Status to P101inout; Internal_Msg_TVPS_Status/send Msg_TVPS_Status to P101inout; Internal_Msg_TDP_Status[D1004in_Con_008400] / send Msg_TDP_Status to P101inout;</div></div></div></div>	<div>This state machine diagram describes the requirements for the following functionalities:</div> <div>- send status messages to the communication partner during initial PDI status report based on information of the internal logic</div>	-		

ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.4072	Def	Initial0		-		
Eu.ILS.4073	Def	/{Initial0 - SENDING_STATUS_MESSAGES}		-		
Eu.ILS.4074	Def	SENDING_STATUS_MESSAGES		-		
Eu.ILS.4131	Def	Internal_Msg_Access_Restriction_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Msg_Access_Restriction_Status to P101inout;{State-internal in SENDING_STATUS_MESSAGES}		-		
Eu.ILS.4135	Def	Internal_Msg_Activation_Zone_Status/send Msg_Activation_Zone_Status to P101inout;{State-internal in SENDING_STATUS_MESSAGES}		-		
Eu.ILS.4136	Def	Internal_Msg_Approach_Zone_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Msg_Approach_Zone_Status to P101inout;{State-internal in SENDING_STATUS_MESSAGES}		-		
Eu.ILS.4138	Def	Internal_Msg_Flank_Protection_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Msg_Flank_Protection_Status to P101inout;{State-internal in SENDING_STATUS_MESSAGES}		-		
Eu.ILS.4139	Def	Internal_Msg_Line_Direction_Control[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Msg_Line_Direction_Control to P101inout;{State-internal in SENDING_STATUS_MESSAGES}		-		
Eu.ILS.4140	Def	Internal_Msg_Line_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Msg_Line_Status to P101inout;{State-internal in SENDING_STATUS_MESSAGES}		-		
Eu.ILS.4141	Def	Internal_Msg_Opposite_Main_Signal_Status[D1001in_Con_007900 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Msg_Opposite_Main_Signal_Status to P101inout;{State-internal in SENDING_STATUS_MESSAGES}		-		
Eu.ILS.4142	Def	Internal_Msg_Route_Monitoring_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Msg_Route_Monitoring_Status to P101inout;{State-internal in SENDING_STATUS_MESSAGES}		-		
Eu.ILS.4132	Def	Internal_Msg_Route_Status/send Msg_Route_Status to P101inout;{State-internal in SENDING_STATUS_MESSAGES}		-		
Eu.ILS.4133	Def	Internal_Msg_Signal_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Msg_Signal_Status to P101inout;{State-internal in SENDING_STATUS_MESSAGES}		-		
Eu.ILS.4134	Def	Internal_Msg_TVPS_Status/send Msg_TVPS_Status to P101inout;{State-internal in SENDING_STATUS_MESSAGES}		-		
Eu.ILS.4261	Def	Internal_Msg_TDP_Status[D1004in_Con_008400]/send Msg_TDP_Status to P101inout;{State-internal in SENDING_STATUS_MESSAGES}		-		
Eu.ILS.4163	Def	when(t5in_Status_Report_Completed)/{SENDING_STATUS - Junction2}		-		
Eu.ILS.3875	Def	PDI_CONNECTION_ESTABLISHED		-		
Eu.ILS.3637	Def	PDI_Connection_Closed/{PDI_CONNECTION_ESTABLISHED - PDI_CONNECTION_CLOSED}		-		
Eu.ILS.4049	Def	RECEIVING_MESSAGES		-		
Eu.ILS.4050	Def	Initial2		-		
Eu.ILS.4051	Def	/{Initial2 - RECEIVING}		-		
Eu.ILS.4052	Def	RECEIVING		-		

ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.4053	Req	<div>Functional Viewpoint - Interface Requirements - Functional Entity STD 1.3</div> <div>stm [Atomic State] RECEIVING [Functional Viewpoint - Interface Requirements - Functional Entity STD 1.3]</div> <div><div><div>●</div><div>Initial0</div><div>↓</div><div>RECEIVING_MESSAGES</div><div><div>Cd_Access_Restriction_Request[D1007in_Con_007400 OR D1001in_Con_007900 OR D1003in_Con_008200 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Cd_Access_Restriction_Request to p3inout; Cd_Flank_Protection_Request[D1007in_Con_007400 OR D1001in_Con_007900 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Cd_Flank_Protection_Request to p3inout; Cd_Route_Cancellation_Request[D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Cd_Route_Cancellation_Request to p3inout; Cd_Route_Prestest_Request[D1007in_Con_007400 OR D1008in_Con_007000]/ send Internal_Cd_Route_Prestest_Request to p3inout; Cd_Route_Release_Inhibition_Activation_Request[D1007in_Con_007400]/ send Internal_Cd_Route_Release_Inhibition_Activation_Request to p3inout; Cd_Route_Request/send Internal_Cd_Route_Request to p3inout; Msg_Access_Restriction_Status[D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Msg_Access_Restriction_Status to p3inout; Msg_Activation_Zone_Status/send Internal_Msg_Activation_Zone_Status to p3inout; Msg_Approach_Zone_Status[D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Msg_Approach_Zone_Status to p3inout; Msg_Flank_Protection_Status[D1007in_Con_007400 OR D1001in_Con_007900 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Msg_Flank_Protection_Status to p3inout; Msg_Line_Direction_Control[D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Msg_Line_Direction_Control to p3inout; Msg_Line_Status[D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900]/ send Internal_Msg_Line_Status to p3inout; Msg_Opposite_Main_Signal_Status[D1001in_Con_007900 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Msg_Opposite_Main_Signal_Status to p3inout; Msg_Route_Monitoring_Status[D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Msg_Route_Monitoring_Status to p3inout; Msg_Route_Prestest_Status[D1007in_Con_007400 OR D1008in_Con_007000]/send Internal_Msg_Route_Prestest_Status to p3inout; Msg_Route_Release_Inhibition_Status[D1007in_Con_007400]/send Internal_Msg_Route_Release_Inhibition_Status to p3inout; Msg_Route_Status/send Internal_Msg_Route_Status to p3inout; Msg_Signal_Status[D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Msg_Signal_Status to p3inout; Msg_Train_Operated_Route_Release_Status/send Internal_Msg_Train_Operated_Route_Release_Status to p3inout; Msg_TVPS_Status/send Internal_Msg_TVPS_Status to p3inout; Cd_Abort_Route_Cancellation_Request[D1004in_Con_008400]/send Internal_Cd_Abort_Route_Cancellation_Request to p3inout; Msg_TDP_Status[D1004in_Con_008400]/send Internal_Msg_TDP_Status to p3inout;</div></div></div></div>	<div>This state machine diagram describes the requirements for the following functionalities:</div> <div>- receive status messages from the communication partner during established PDI connection and forward them to the internal logic</div> <div>- receive commands from the communication partner during established PDI connection and forward them to the internal logic</div>	-		
Eu.ILS.4054	Def	Initial0		-		
Eu.ILS.4055	Def	/{Initial0 - RECEIVING_MESSAGES}		-		
Eu.ILS.4056	Def	RECEIVING_MESSAGES		-		
Eu.ILS.4077	Def	Cd_Access_Restriction_Request[D1007in_Con_007400 OR D1001in_Con_007900 OR D1003in_Con_008200 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Cd_Access_Restriction_Request to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4088	Def	Cd_Flank_Protection_Request[D1007in_Con_007400 OR D1001in_Con_007900 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Internal_Cd_Flank_Protection_Request to p3inout;{State-internal in RECEIVING_MESSAGES}		-		

ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.4091	Def	Cd_Route_Cancellation_Request[D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Internal_Cd_Route_Cancellation_Request to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4092	Def	Cd_Route_Pretest_Request[D1007in_Con_007400 OR D1008in_Con_007000]/send Internal_Cd_Route_Pretest_Request to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4093	Def	Cd_Route_Release_Inhibition_Activation_Request[D1007in_Con_007400]/send Internal_Cd_Route_Release_Inhibition_Activation_Request to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4094	Def	Cd_Route_Request/send Internal_Cd_Route_Request to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4095	Def	Msg_Access_Restriction_Status[D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Internal_Msg_Access_Restriction_Status to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4096	Def	Msg_Activation_Zone_Status/send Internal_Msg_Activation_Zone_Status to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4097	Def	Msg_Approach_Zone_Status[D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Internal_Msg_Approach_Zone_Status to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4079	Def	Msg_Flank_Protection_Status[D1007in_Con_007400 OR D1001in_Con_007900 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Internal_Msg_Flank_Protection_Status to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4080	Def	Msg_Line_Direction_Control[D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Internal_Msg_Line_Direction_Control to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4081	Def	Msg_Line_Status[D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900]/send Internal_Msg_Line_Status to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4082	Def	Msg_Opposite_Main_Signal_Status[D1001in_Con_007900 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Internal_Msg_Opposite_Main_Signal_Status to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4083	Def	Msg_Route_Monitoring_Status[D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Internal_Msg_Route_Monitoring_Status to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4084	Def	Msg_Route_Pretest_Status[D1007in_Con_007400 OR D1008in_Con_007000]/send Internal_Msg_Route_Pretest_Status to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4085	Def	Msg_Route_Release_Inhibition_Status[D1007in_Con_007400]/send Internal_Msg_Route_Release_Inhibition_Status to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4086	Def	Msg_Route_Status/send Internal_Msg_Route_Status to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4087	Def	Msg_Signal_Status[D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Internal_Msg_Signal_Status to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4089	Def	Msg_Train_Operated_Route_Release_Status/send Internal_Msg_Train_Operated_Route_Release_Status to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4090	Def	Msg_TVPS_Status/send Internal_Msg_TVPS_Status to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4191	Def	Cd_Abort_Route_Cancellation_Request[D1004in_Con_008400]/send Internal_Cd_Abort_Route_Cancellation_Request to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4262	Def	Msg_TDP_Status[D1004in_Con_008400]/send Internal_Msg_TDP_Status to p3inout;{State-internal in RECEIVING_MESSAGES}		-		
Eu.ILS.4057	Def	SENDING_MESSAGES		-		
Eu.ILS.4058	Def	Initial3		-		
Eu.ILS.4059	Def	/ {Initial3 - SENDING}		-		
Eu.ILS.4060	Def	SENDING		-		

ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.4061	Req	<div>Functional Viewpoint - Interface Requirements - Functional Entity STD 1.4</div> <div><div>stm [Atomic State] SENDING [Functional Viewpoint - Interface Requirements - Functional Entity STD 1.4]</div><div><div><div>Initial0</div><div>SENDING_MESSAGES</div><div>Internal_Cd_Access_Restriction_Request[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1003in_Con_008200 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Cd_Access_Restriction_Request to P101inout; Internal_Cd_Flank_Protection_Request[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Cd_Flank_Protection_Request to P101inout; Internal_Cd_Route_Cancellation_Request[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Cd_Route_Cancellation_Request to P101inout; Internal_Cd_Route_Prestest_Request[D1007in_Con_007400 OR D1008in_Con_007000]/send Cd_Route_Prestest_Request to P101inout; Internal_Cd_Route_Release_Inhibition_Activation_Request[D1007in_Con_007400]/send Cd_Route_Release_Inhibition_Activation_Request to P101inout; Internal_Cd_Route_Request/send Cd_Route_Request to P101inout; Internal_Msg_Access_Restriction_Status[D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Msg_Access_Restriction_Status to P101inout; Internal_Msg_Activation_Zone_Status/send Msg_Activation_Zone_Status to P101inout; Internal_Msg_Approach_Zone_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Msg_Approach_Zone_Status to P101inout; Internal_Msg_Flank_Protection_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Msg_Flank_Protection_Status to P101inout; Internal_Msg_Line_Direction_Control[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Msg_Line_Direction_Control to P101inout; Internal_Msg_Line_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Msg_Line_Status to P101inout; Internal_Msg_Opposite_Main_Signal_Status[D1001in_Con_007900 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Msg_Opposite_Main_Signal_Status to P101inout; Internal_Msg_Route_Monitoring_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Msg_Route_Monitoring_Status to P101inout; Internal_Msg_Route_Prestest_Status[D1007in_Con_007400 OR D1008in_Con_007000]/send Msg_Route_Prestest_Status to P101inout; Internal_Msg_Route_Release_Inhibition_Status[D1007in_Con_007400]/send Msg_Route_Release_Inhibition_Status to P101inout; Internal_Msg_Route_Status/send Msg_Route_Status to P101inout; Internal_Msg_Signal_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Msg_Signal_Status to P101inout; Internal_Msg_Train_Operated_Route_Release_Status/send Msg_Train_Operated_Route_Release_Status to P101inout; Internal_Msg_TVPS_Status/send Msg_TVPS_Status to P101inout; Internal_Cd_Abort_Route_Cancellation_Request[D1004in_Con_008400]/send Cd_Abort_Route_Cancellation_Request to P101inout; Internal_Msg_TDP_Status[D1004in_Con_008400]/send Msg_TDP_Status to P101inout;</div></div></div><div>This state machine diagram describes the requirements for the following functionalities:  - send status messages to the communication partner during established PDI connection based on information of the internal logic - send commands to the communication partner during established PDI connection based on information of the internal logic</div></div>	-			
Eu.ILS.4062	Def	Initial0		-		
Eu.ILS.4063	Def	/{Initial0 - SENDING_MESSAGES}		-		
Eu.ILS.4064	Def	SENDING_MESSAGES		-		
Eu.ILS.4098	Def	Internal_Cd_Access_Restriction_Request[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1003in_Con_008200 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Cd_Access_Restriction_Request to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4109	Def	Internal_Cd_Flank_Protection_Request[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Cd_Flank_Protection_Request to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4112	Def	Internal_Cd_Route_Cancellation_Request[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/ send Cd_Route_Cancellation_Request to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4113	Def	Internal_Cd_Route_Prestest_Request[D1007in_Con_007400 OR D1008in_Con_007000]/send Cd_Route_Prestest_Request to P101inout;{State-internal in SENDING_MESSAGES}		-		



ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.4114	Def	Internal_Cd_Route_Release_Inhibition_Activation_Request[D1007in_Con_007400]/send Cd_Route_Release_Inhibition_Activation_Request to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4115	Def	Internal_Cd_Route_Request/send Cd_Route_Request to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4116	Def	Internal_Msg_Access_Restriction_Status[D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Msg_Access_Restriction_Status to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4117	Def	Internal_Msg_Activation_Zone_Status/send Msg_Activation_Zone_Status to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4118	Def	Internal_Msg_Approach_Zone_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Msg_Approach_Zone_Status to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4100	Def	Internal_Msg_Flank_Protection_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Msg_Flank_Protection_Status to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4101	Def	Internal_Msg_Line_Direction_Control[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Msg_Line_Direction_Control to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4102	Def	Internal_Msg_Line_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Msg_Line_Status to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4103	Def	Internal_Msg_Opposite_Main_Signal_Status[D1001in_Con_007900 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Msg_Opposite_Main_Signal_Status to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4104	Def	Internal_Msg_Route_Monitoring_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Msg_Route_Monitoring_Status to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4105	Def	Internal_Msg_Route_Prestest_Status[D1007in_Con_007400 OR D1008in_Con_007000]/send Msg_Route_Prestest_Status to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4106	Def	Internal_Msg_Route_Release_Inhibition_Status[D1007in_Con_007400]/send Msg_Route_Release_Inhibition_Status to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4107	Def	Internal_Msg_Route_Status/send Msg_Route_Status to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4108	Def	Internal_Msg_Signal_Status[ D1007in_Con_007400 OR D1001in_Con_007900 OR D1002in_Con_008000 OR D1003in_Con_008200 OR D1004in_Con_008400 OR D1005in_Con_008800 OR D1006in_Con_310900 OR D1008in_Con_007000]/send Msg_Signal_Status to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4110	Def	Internal_Msg_Train_Operated_Route_Release_Status/send Msg_Train_Operated_Route_Release_Status to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4111	Def	Internal_Msg_TVPS_Status/send Msg_TVPS_Status to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4192	Def	Internal_Cd_Abort_Route_Cancellation_Request[D1004in_Con_008400]/send Cd_Abort_Route_Cancellation_Request to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.4263	Def	Internal_Msg_TDP_Status[D1004in_Con_008400]/send Msg_TDP_Status to P101inout;{State-internal in SENDING_MESSAGES}		-		
Eu.ILS.1839	Info	S_SCI_ILS_National	The functional entity S_SCI_ILS_National represents the IM specific part of the interface. It complements the functional entity S_SCI_ILS and shall be defined by national requirements. An instance of S_SCI_ILS_National represents one boundary.	-		
Eu.ILS.3650	Req	<div><div>[Block] S_SCI_ILS_National [Functional Viewpoint - Interface Requirements - Functional Entity]</div><div><div><div><div><div>ibdd [Block] S_SCI_ILS_National [Functional Viewpoint - Interface Requirements - Functional Entity]</div></div><div><div><div><div>«functional entity» S_SCI_ILS_National</div><div><div><div><div><div><div>t8in_Start_Status_Report : PulsedIn</div><div>t5out_Status_Report_Completed : PulsedOut</div><div>t6in_Check_Adj_Status : PulsedIn</div><div>t7out_Adj_Status_Report_Complete : PulsedOut</div></div></div><div><div><div><div>p3inout : ~SCI_ILS_Internal</div></div></div></div></div></div></div></div></div></div></div></div></div></div>		-		
Eu.ILS.4075	Def	p3inout		-		
Eu.ILS.1848	Def	t5out_Status_Report_Completed		-		
Eu.ILS.3839	Def	t6in_Check_Adj_Status		-		
Eu.ILS.3840	Def	t7out_Adj_Status_Report_Complete		-		
Eu.ILS.3891	Def	t8in_Start_Status_Report		-		
Eu.ILS.1304	Head	4 Configuration requirements		Default		
Eu.ILS.1310	Info	The SCI-ILS uses several configurable interface elements which shall be used. These are described below.		Default		
Eu.ILS.1318	Head	4.1 Boundary		Default		



ID	Type	Requirement Part 1	Requirement Part 2	Appl.	JIRA	V 4.3 (1.A) > V 4.3 (0.A)
Eu.ILS.1308	Info	A boundary is a configurable interface element which marks the interlocking system boundary for one track. It represents the end of one interlocking system area and the beginning of the other interlocking system area. The boundary is located at the end of the TVPS of the one interlocking and at the beginning of the TVPS of the other interlocking.		Default		
Eu.ILS.1320	Req	Boundaries shall be unambiguously identifiable with an identifier defined by national requirements. Further information about the identifier is given in [Eu.Doc.42].		Default		
Eu.ILS.1319	Req	One boundary shall be defined and configured for each track which leaves the area of an interlocking system and enters the area of the adjacent interlocking system.		Default		
Eu.ILS.1328	Req	The following track and interface elements shall be unambiguously identifiable with a boundary, because a boundary is needed to address these uniquely for each track. <ul style="list-style-type: none"><li>• direction</li><li>• access restriction to the track section</li><li>• line status message for adjacent interlocking</li><li>• flank protection element adjacent to the boundary</li><li>• signal in advance of the boundary</li><li>• TVPS adjacent to the boundary</li></ul>		Default		
Eu.ILS.4006	Req	The following track and interface elements shall be unambiguously identifiable with a boundary combined with an unique identifier of the operational element. <ul style="list-style-type: none"><li>• route</li><li>• overlap</li><li>• activation zone for level crossing across the boundary</li><li>• approach zone status message for adjacent interlocking</li><li>• route monitoring message for adjacent interlocking</li></ul>		Default		
Eu.ILS.1321	Info	Further general information about interlocking system boundaries given in chapter 7 of [Eu.Doc.10].		Default		
Eu.ILS.1340	Head	<b>4.2 Direction</b>		Default		
Eu.ILS.1327	Info	The direction is a configurable interface element which defines the direction for one track across one boundary.		Default		
Eu.ILS.1341	Req	Directions shall be used for applications on the line only.		Default		
Eu.ILS.1342	Req	The following values for the direction shall be used for configuration. <ul style="list-style-type: none"><li>• entry</li><li>• exit</li></ul>		Default		
Eu.ILS.1324	Head	<b>4.3 Route</b>		Default		
Eu.ILS.1307	Req	A route shall be identified unambiguously with an identifier defined by national requirements. Further information about the identifier is given in [Eu.Doc.42].		Default		
Eu.ILS.1325	Head	<b>4.4 Overlap</b>		Default		
Eu.ILS.1309	Req	An overlap shall be identified unambiguously with an identifier defined by national requirements. Further information about the identifier is given in [Eu.Doc.42].		Default		
Eu.ILS.1322	Head	<b>4.5 Activation Zone</b>		Default		
Eu.ILS.1305	Req	An activation zone shall be identified unambiguously with an identifier defined by national requirements. Further information about the identifier is given in [Eu.Doc.42].		Default		
Eu.ILS.1323	Head	<b>4.6 Approach Zone</b>		Default		
Eu.ILS.1306	Req	An approach zone shall be identified unambiguously with an identifier defined by national requirements. Further information about the identifier is given in [Eu.Doc.42].		Default		